

FIG. 38

IMAGE DETECTION INTERFACE

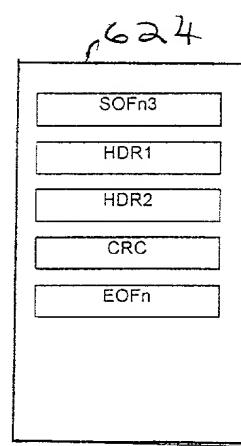
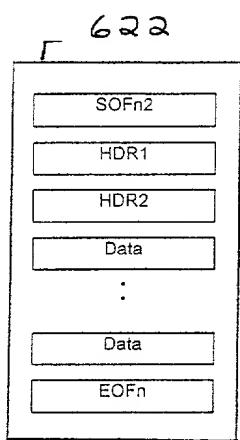
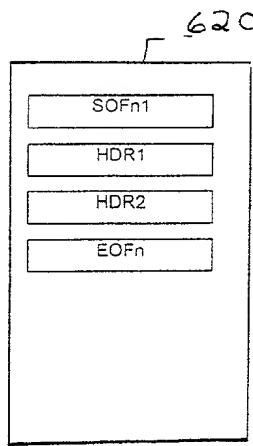


FIG. [REDACTED]
39

FIG. [REDACTED]
40

FIG. [REDACTED]
41

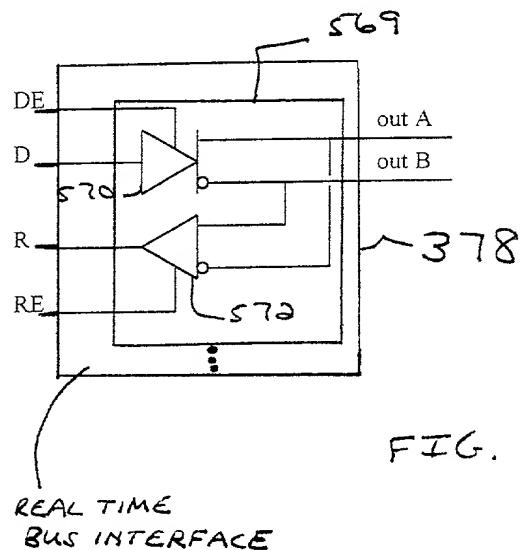
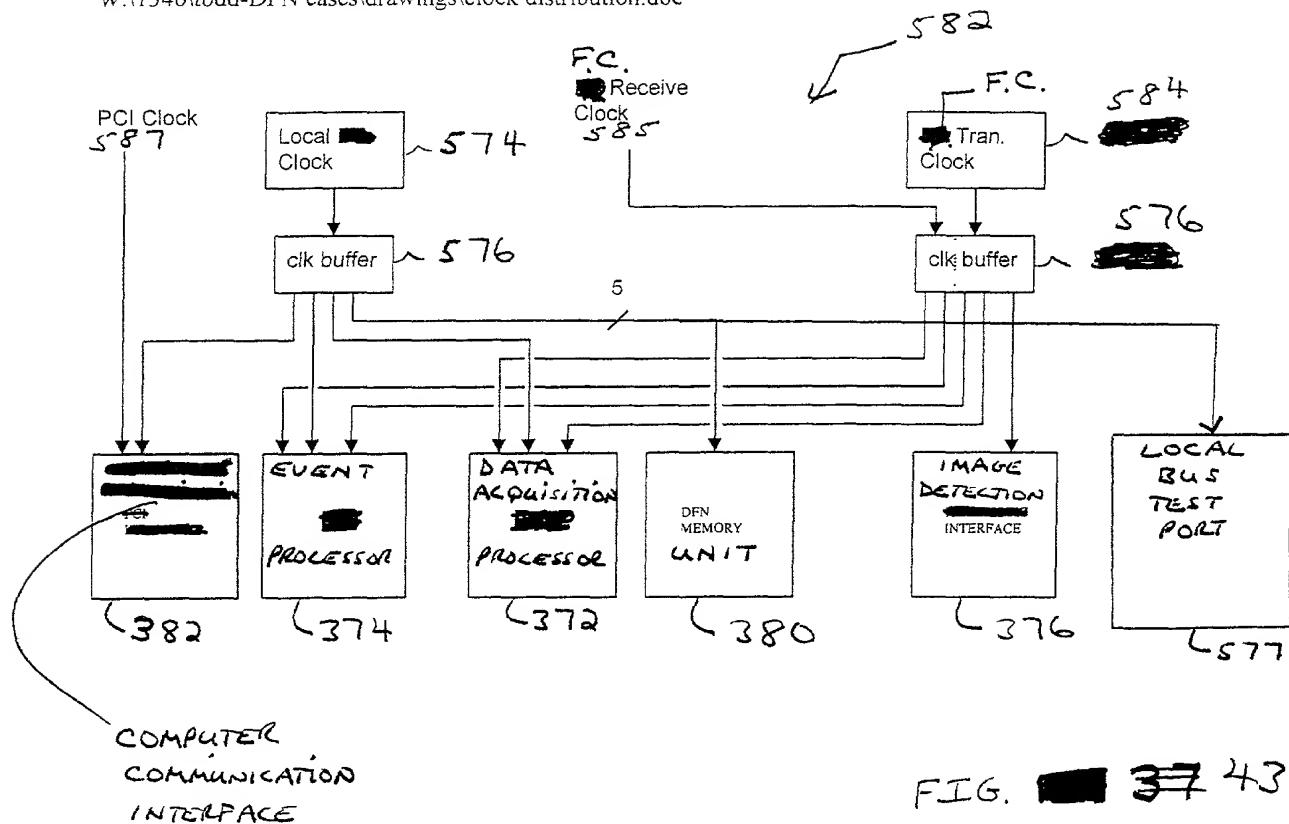


FIG. [REDACTED] 42



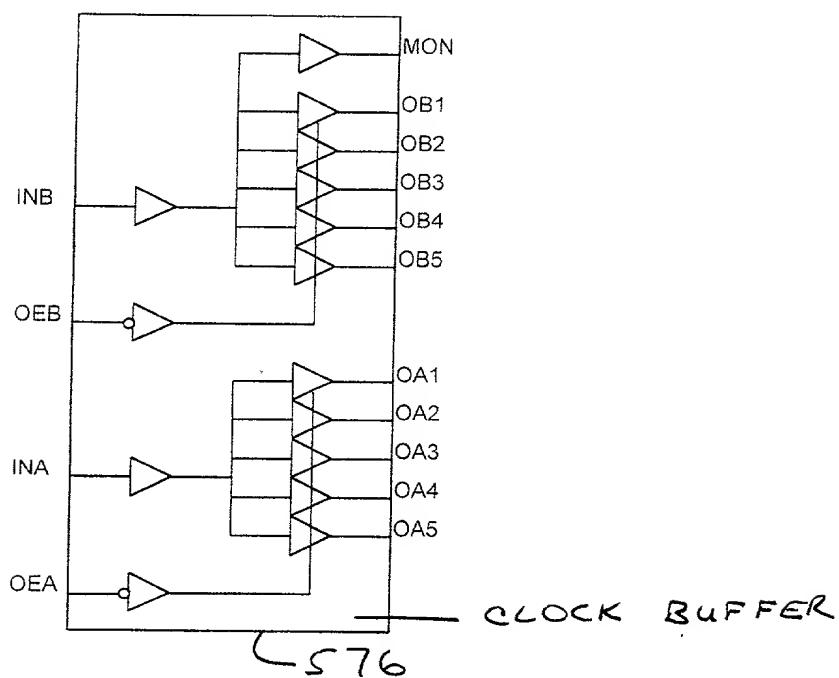
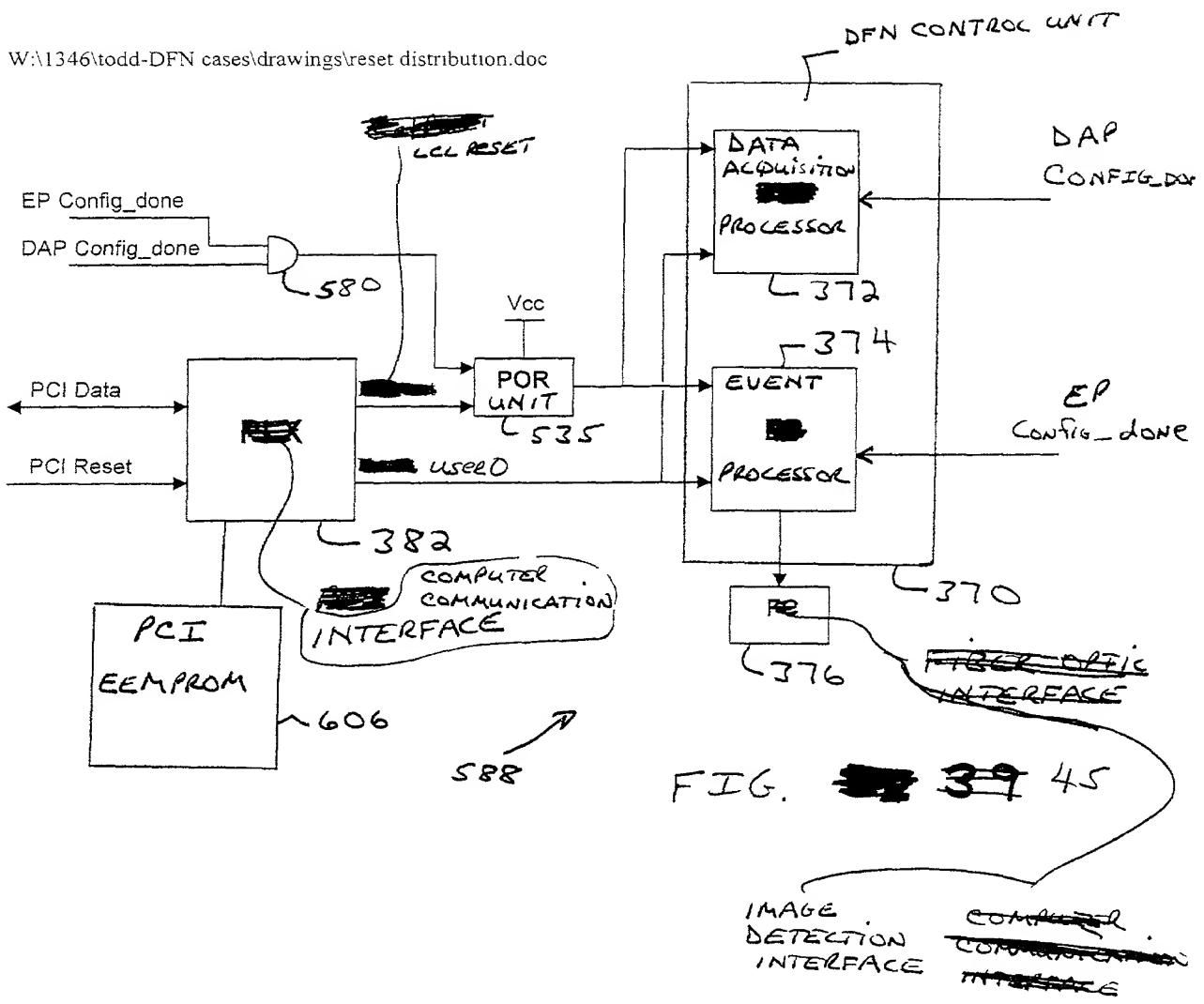
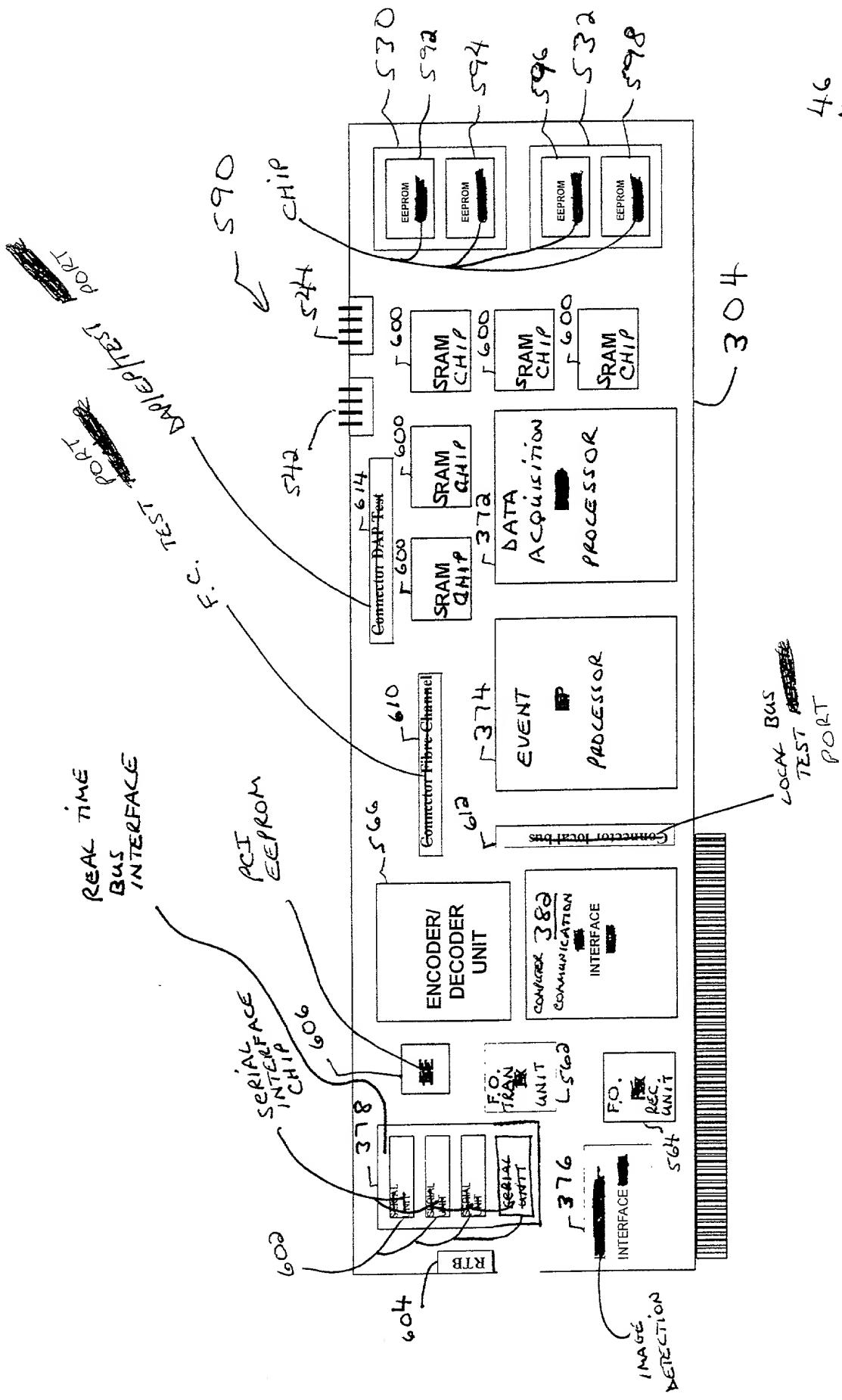
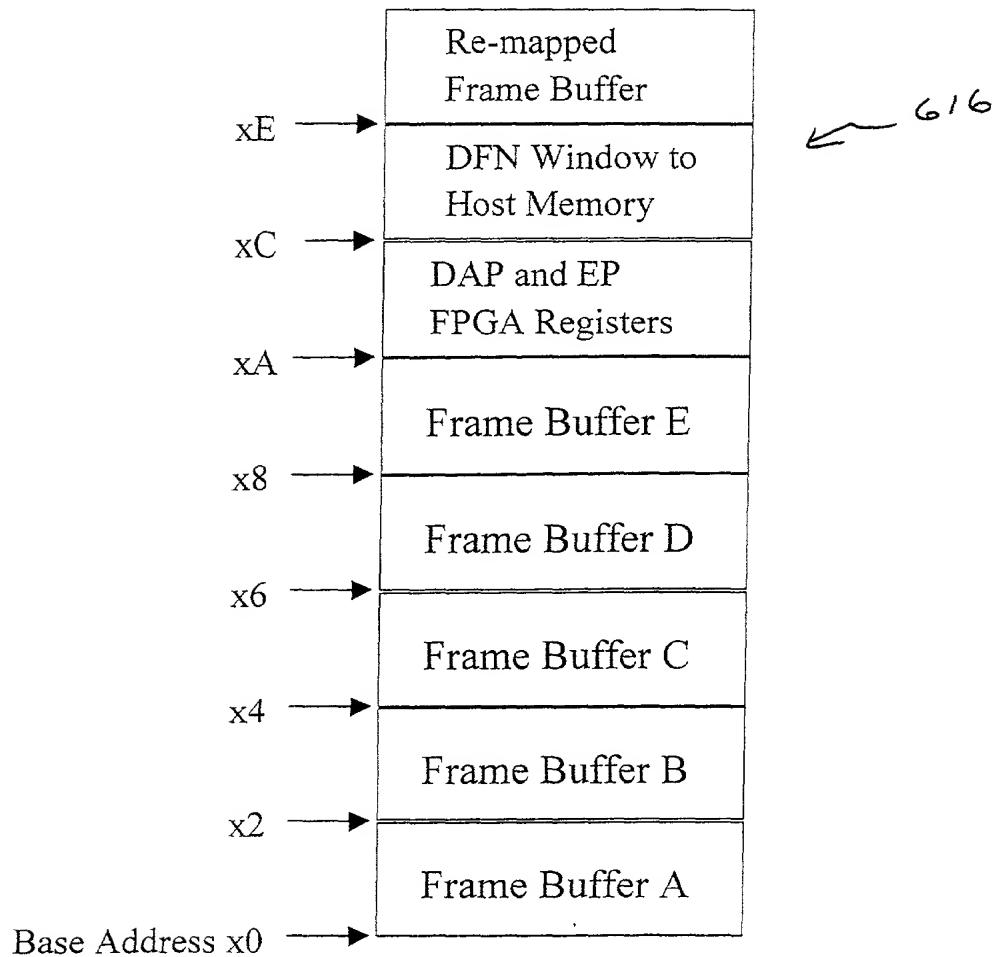


FIG. [REDACTED] 44







Mapping of 16 MByte PCI Address Space

FIG. 47

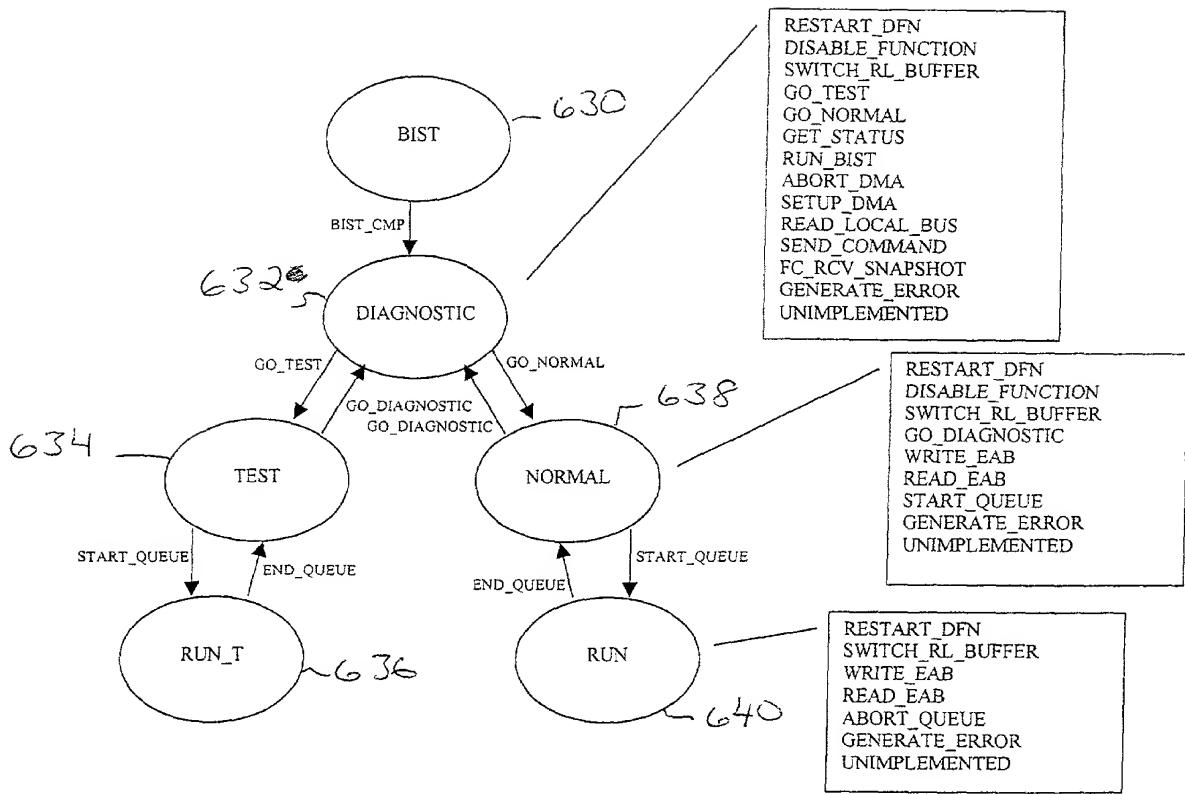


FIG. 48

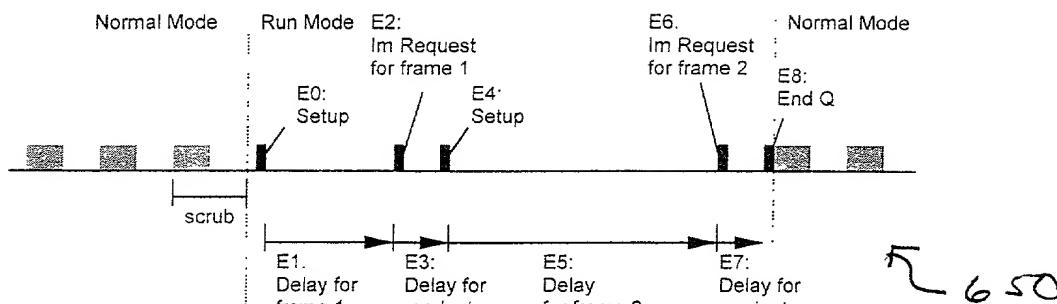


FIG. 49

| Event Mnemonic | Event (showing size of arguments) | Op Code (hex) | Data (bytes) | Total (bytes) |
|--------------------------|---|---------------------|-----------------|------------------|
| Endq | Endq | 14 | 0 | 1 |
| Delay (T) | Delay (0xff ff ff ff) | 10 | 4 | 5 |
| Send (command, value) | Send (0xff ff ff ff, 0xff ff ff ff) | 04 | 8 | 9 |
| LoopKN (K, N) | LoopKN (0xff ff, 0xff) | 0C | 3 | 4 |
| LoopKF (K, F) | LoopKF (0xff ff, 0xff ff ff) | 0D | 5 | 6 |
| Wait (F) | Wait (0xff ff ff) | 09 | 3 | 4 |
| Flag (F) | Flag (0xff ff ff) | 08 | 3 | 4 |

FIG. 50

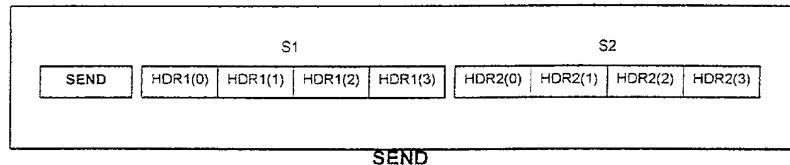


FIG. 51

| Error Mnemonic | Description of Error |
|---------------------|---|
| FC_TIMEOUT | Timeout expired with no ACK detected |
| FC_BAD_ACK | ACK did not match transmitted command |
| FC_EXTRA_ACK | Unexpected ACK received |
| FC_EXTRA_CMD | New Send event while waiting for ACK from previous Send |
| SIG_DETN | No input signal power on Fibre Channel (cable disconnected?) |
| RXERROR | Fibre Channel receiver detected bad data (defective chipset?) |
| WRDSYNCN | Fibre Channel Data link unsynchronized |
| CRXS(1) | Bad Received CRC detected (Fiber-optic cable problem?) |
| CRXS(3) and CRXS(2) | Bad order in link state machine (defective chipset?) |

← 672

FIG. [REDACTED] 52

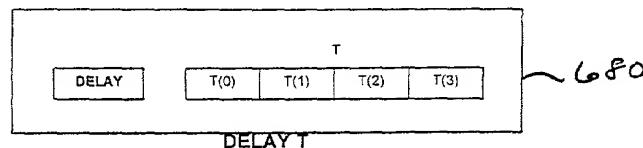


FIG. [REDACTED] 53

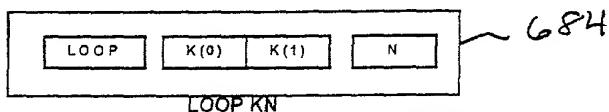
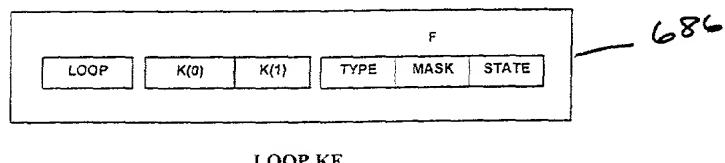
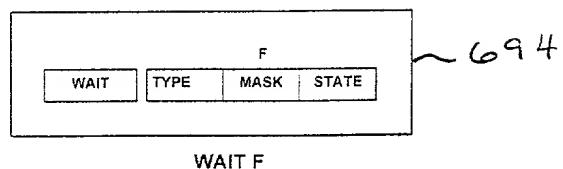


FIG. [REDACTED] 54



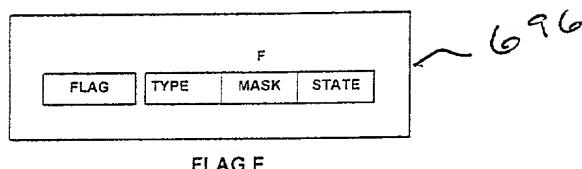
LOOP KF

FIG. [REDACTED] 55



~ 694

FIG. [REDACTED] 56



~ 696

FIG. [REDACTED] 57



~ 697

FIG. [REDACTED] 58

~ 698

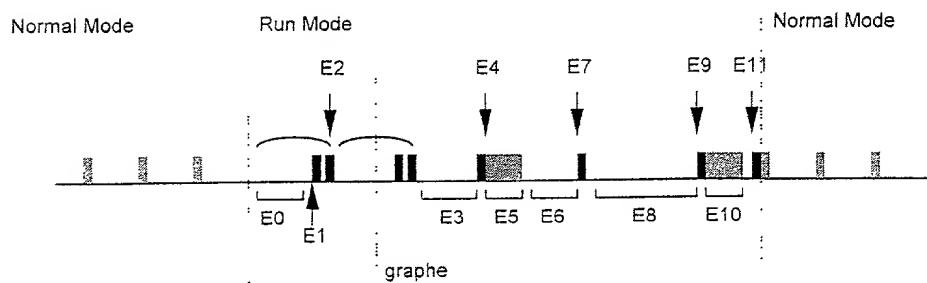


FIG. [REDACTED] 59

| | |
|-----|-----------------|
| E11 | EndQ |
| E10 | Delay 125 ms |
| E9 | Send Im Request |
| E8 | Delay 500 ms |
| E7 | Flag RT2 |
| E6 | Delay 50 ms |
| E5 | Delay 125 ms |
| E4 | Send Im Request |
| E3 | Delay 300 ms |
| E2 | Loop 2, RT1 |
| E1 | Send Scrub |
| E0 | Delay 300 ms |

Event Queue

700

FIG. 60

702

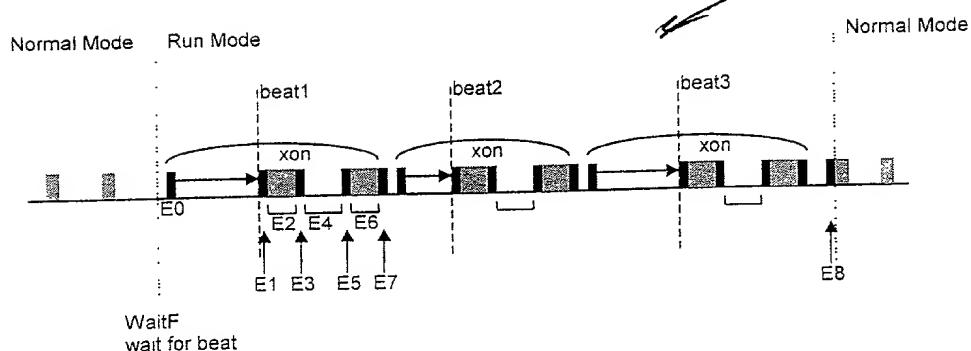


FIG. 61

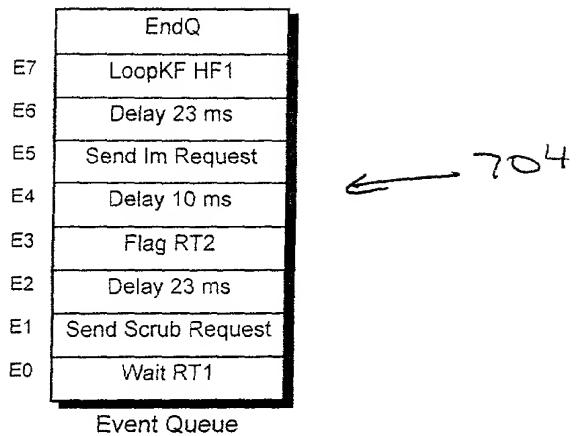


FIG. 62

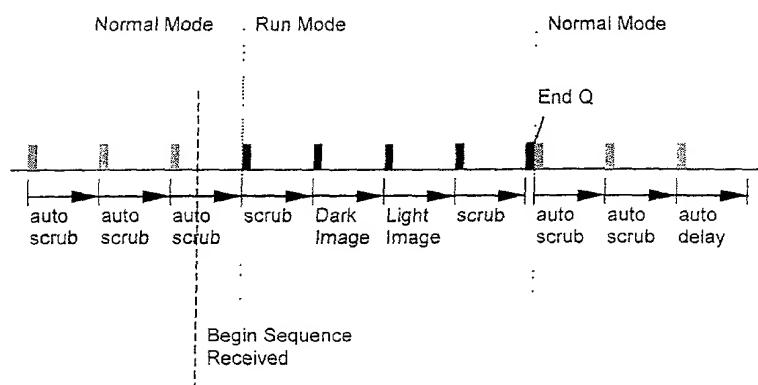


FIG. 63

```

sequence_begin ();

# define qv defaults:
%qvl =('delay_qv' => 5000);

# call frame with qv's
frame_type1 (NULL, \%qvl, 1);

sequence_end ();

```

FIG. 64

```

sub frame
{
    $QVf = 'frame';

    %qv = ('delay_qv' => [10000]);
    %qp = ();

    compile_init(@_, \%qp, \%qv, $QVf);

    Delay('delay_qv');

    compile_finit();
}

```

FIG. 65

```

pDFN->DFNChangeQueueVariable
(
    (char *)SymName,           // variable name
    (char *)sndBuf,            // new value
    BufSize,                  // num bytes to write
    (ULONG *)&debug           // developer info
);

```

FIG. 66

User Application

```

// load and run the event sequence
pDFN->DFNBeginSequenceNoMappingNoLog
(snum, "d:\\HF.bin");

//assign data to be passed
sndBuf = 25000;

// change the queue variable
pDFN->DFNChangeQueueVariable
(
    (char *)SymName,           // variable name
    (char *)sndBuf,            // new value
    (ULONG)sizeof sndBuf,     // num bytes to write
    (ULONG *)&debug           // developer info
);

```

FIG. 67

Perl Script

```

sub frame_type1
{
    $HFfrm = 'frame_type1';

    %qv = ('delay_qv' => [20000]);
    %qp = ();

    $image_cmd = [0x800000, 0x0];

    compile_init(@_, \%qp, \%qv, $HFfrm);

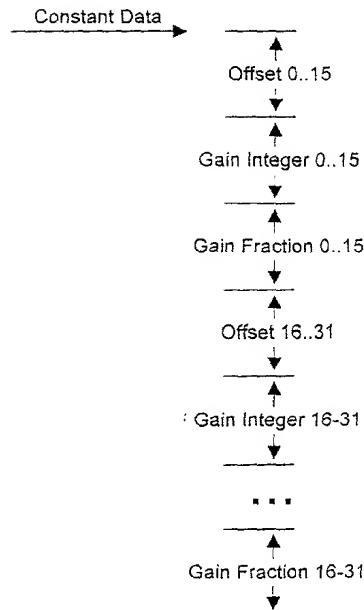
    Send($image_cmd);
    Delay('delay_qv');
    LoopKF(2, 0xAFF01);

    compile_finit();
}

```

FIG. 68

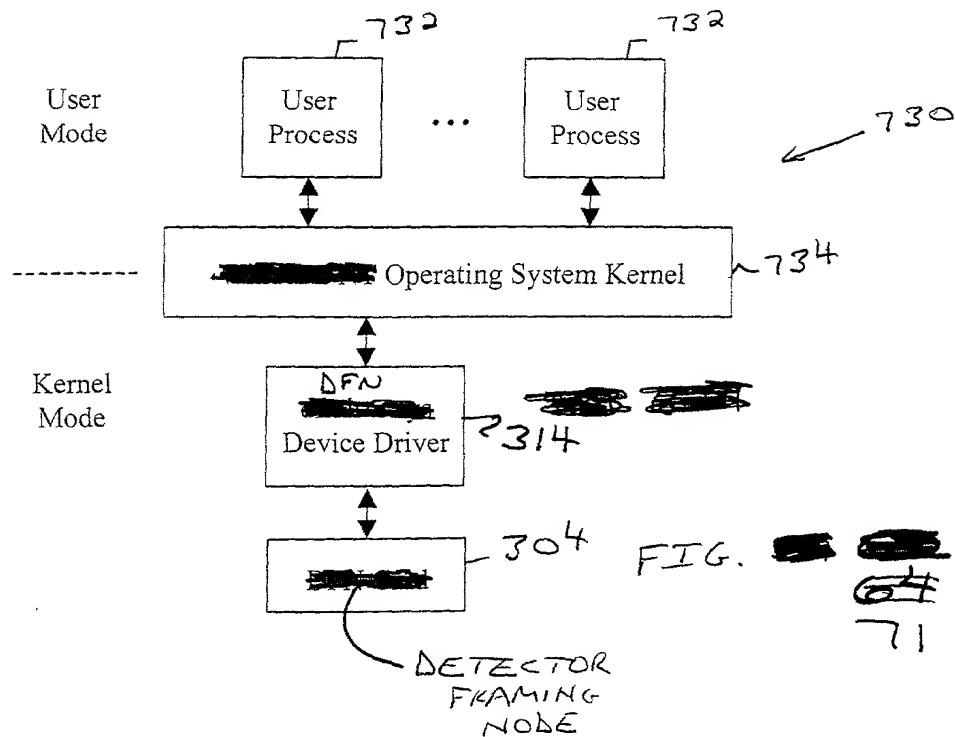
44



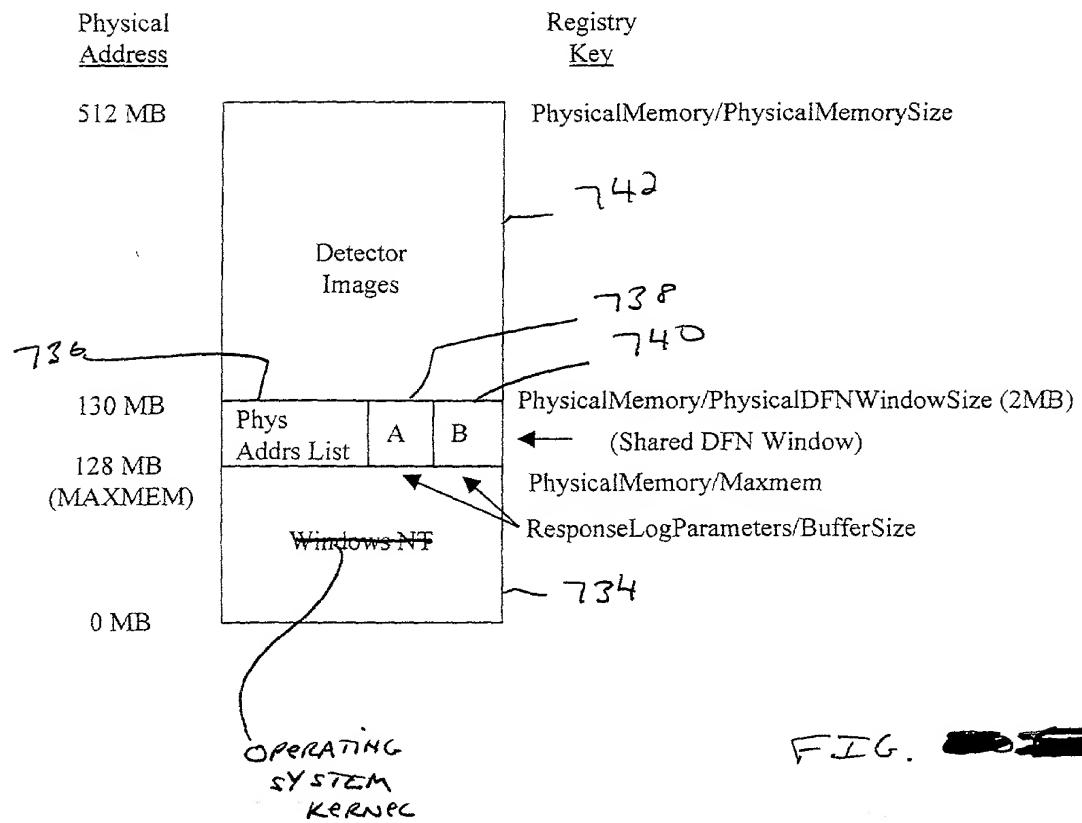
Constant Memory Format

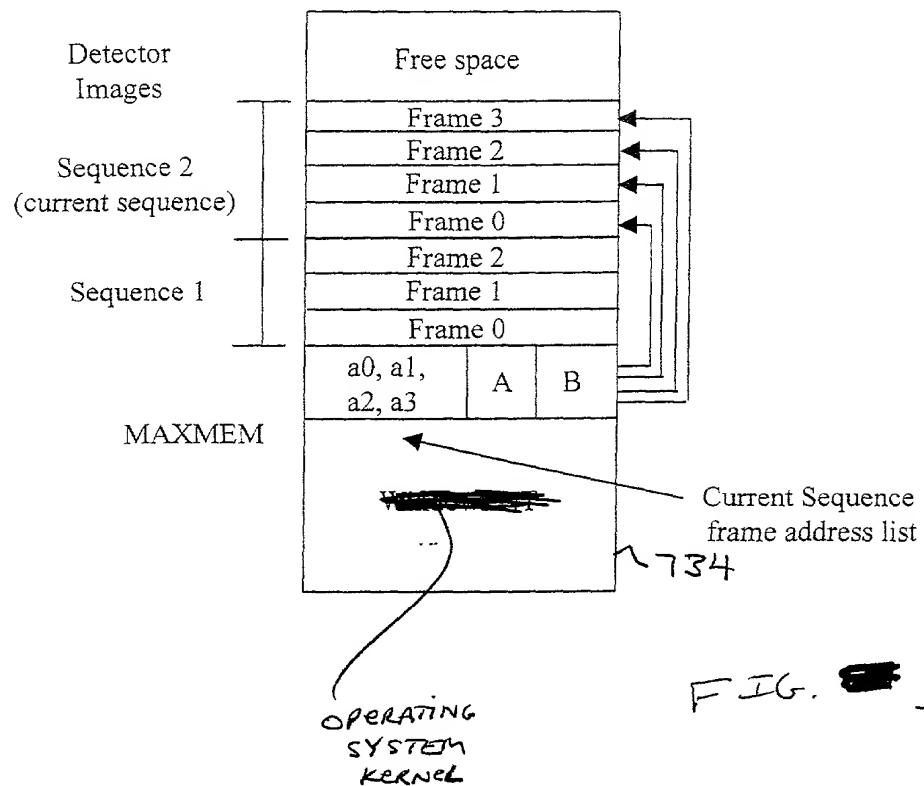
FIG. [REDACTED]
[REDACTED]
[REDACTED]
70

45



0 MB 128 MB (MAXMEM) 130 MB 512 MB





PLATEAU SYSTEM
IMAGING SYSTEM

NOTE: THIS Dwg. S/B
ENHANCED, AS PER,
E.G. 6, OSS, 295

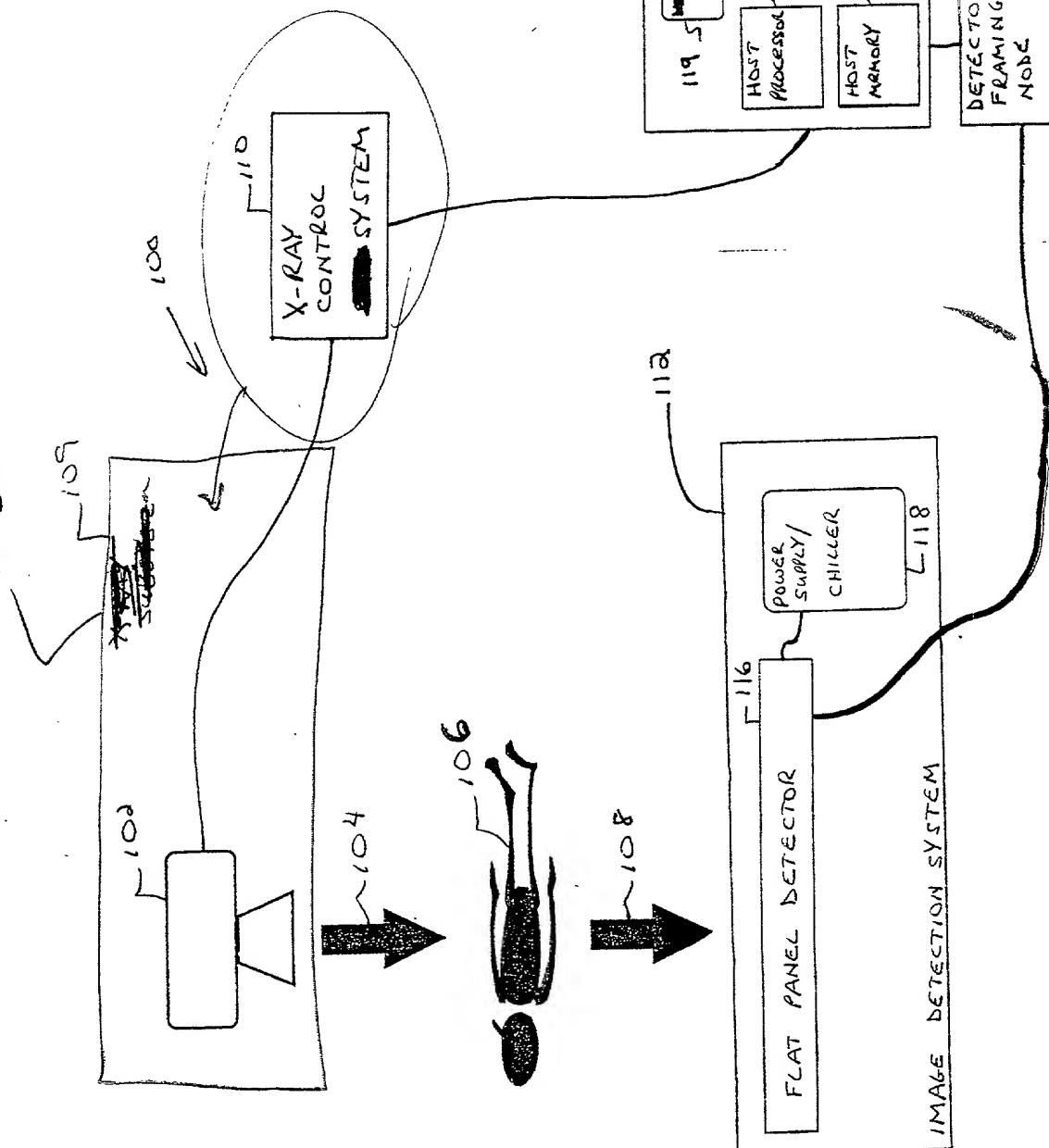
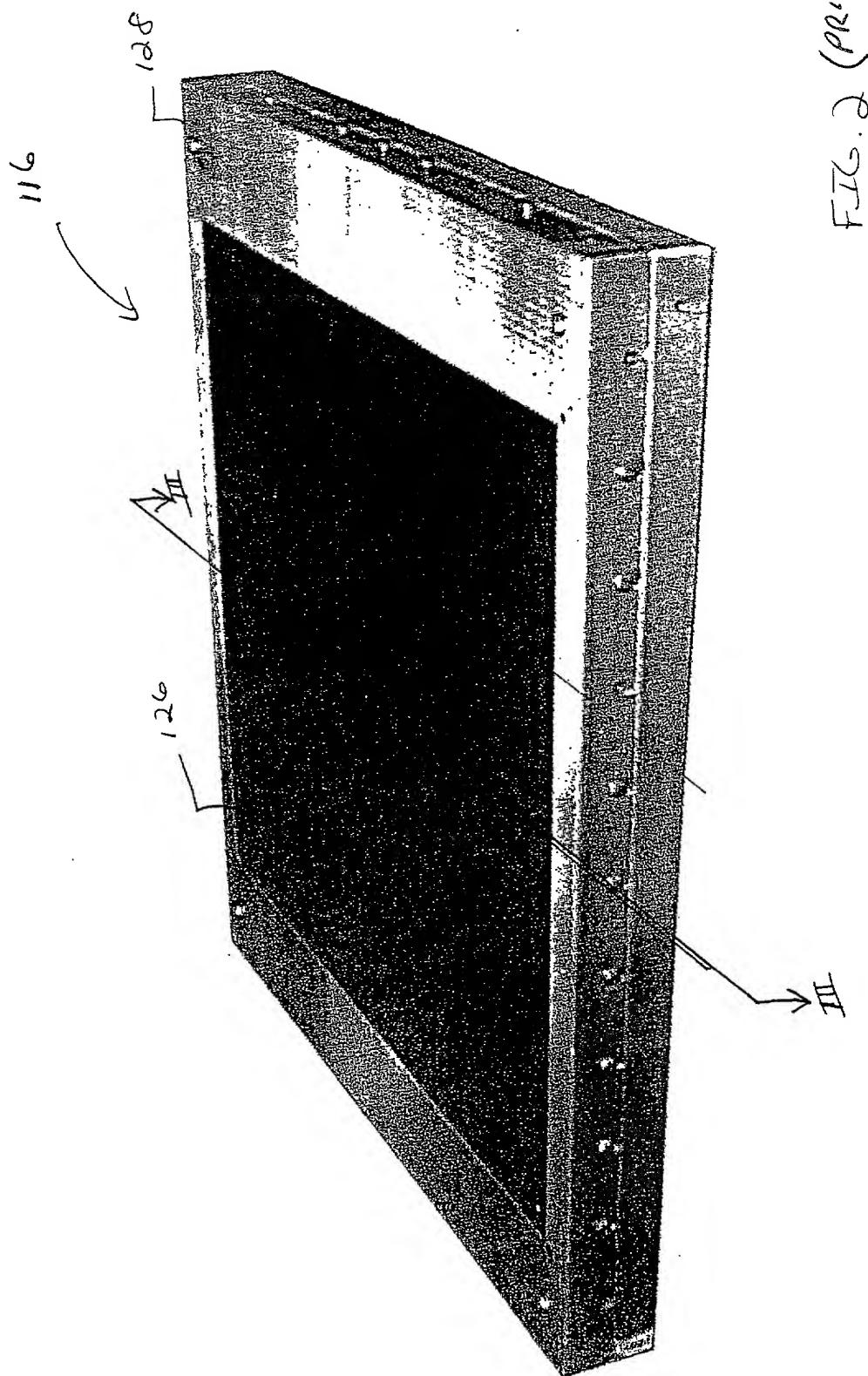
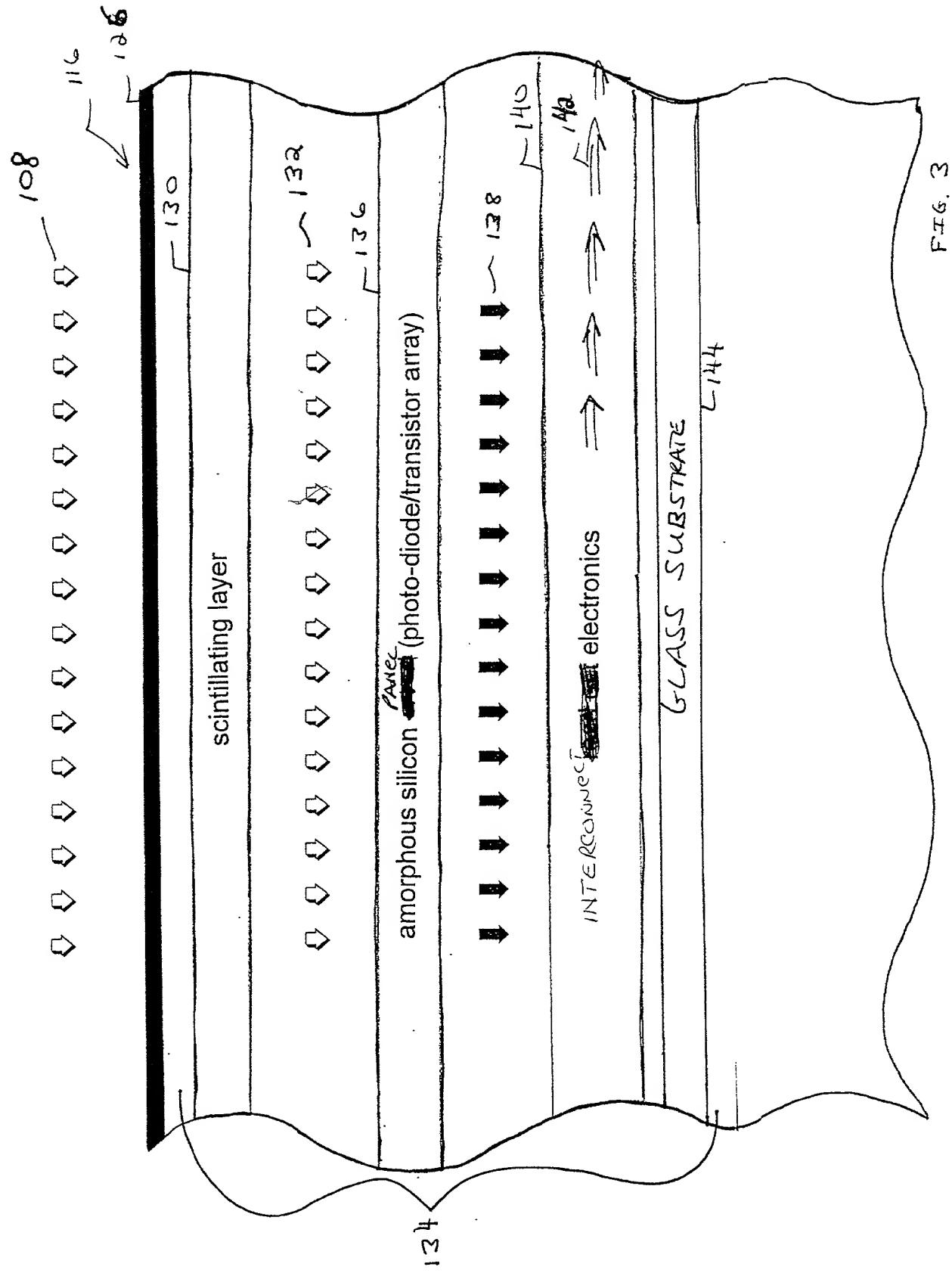


FIG. 1





FIGS. 3

FIG. 4
(prior art)

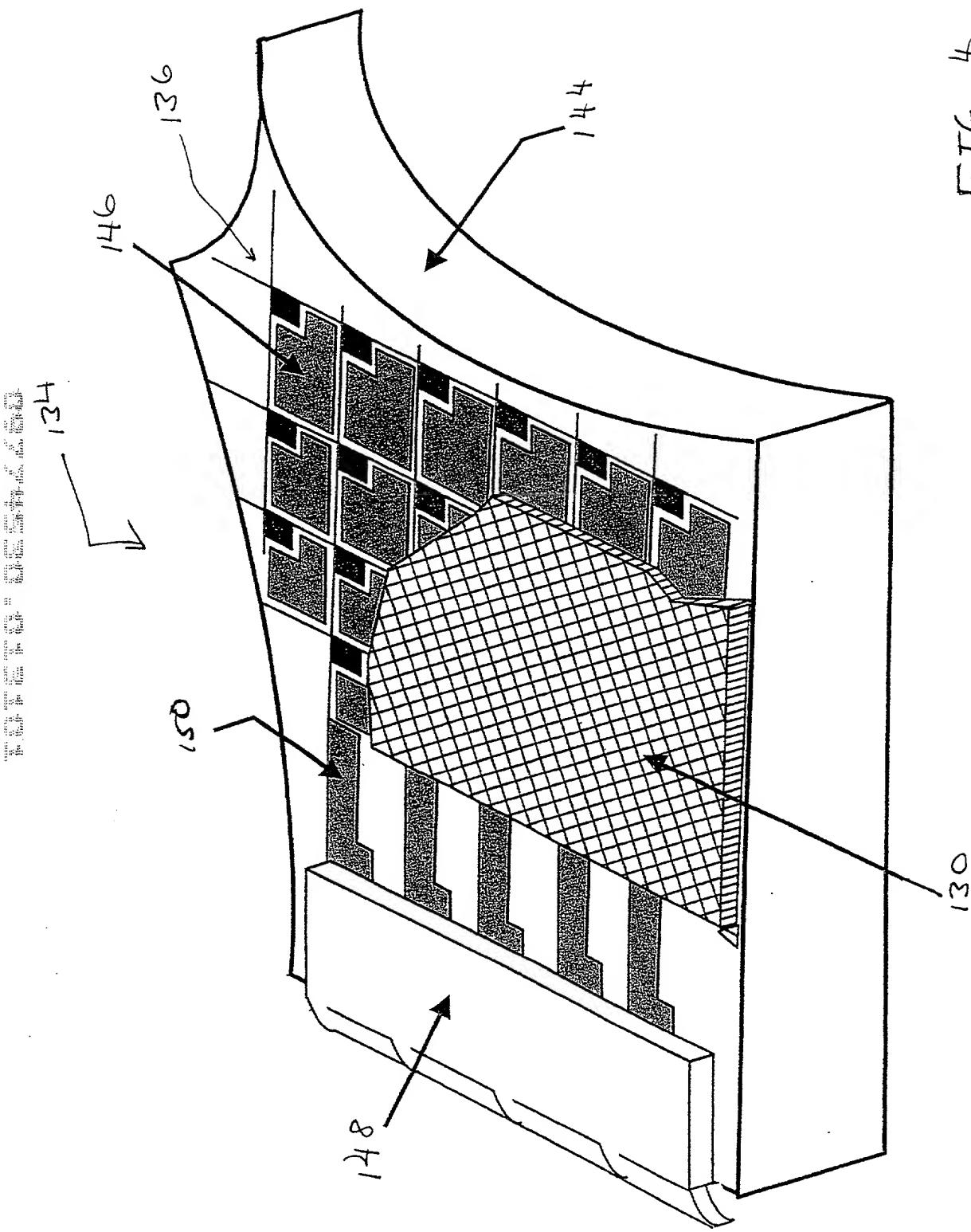
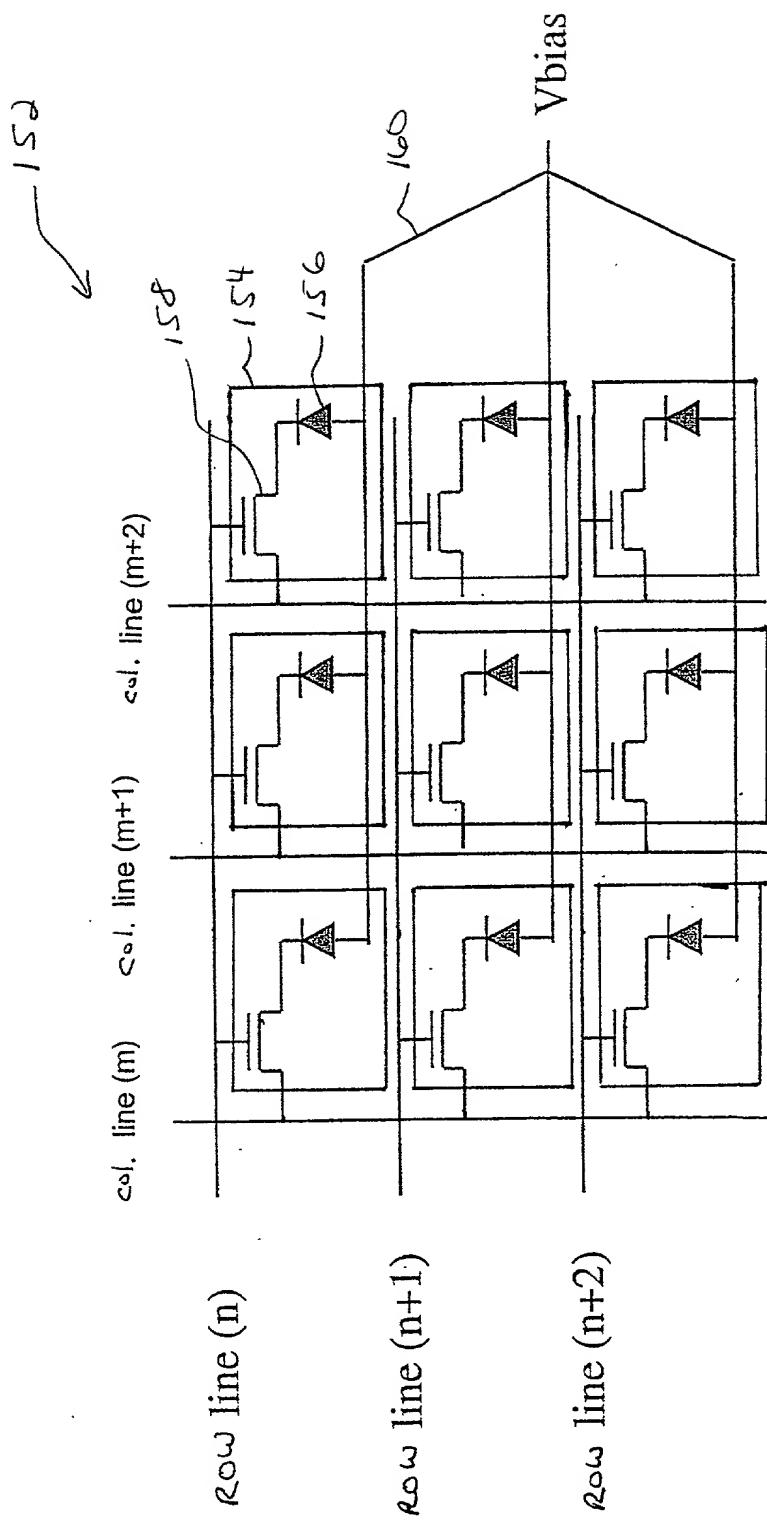
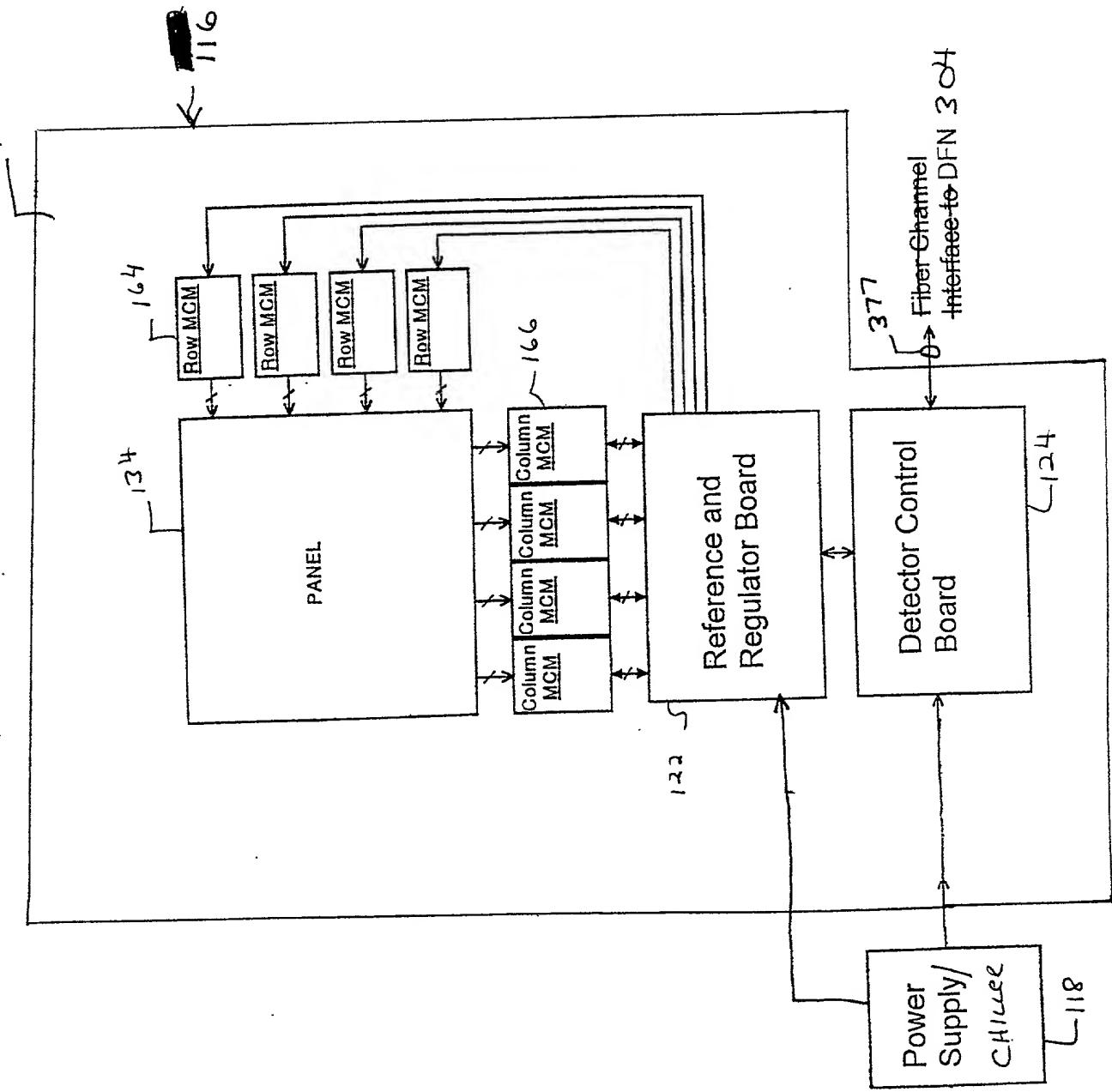


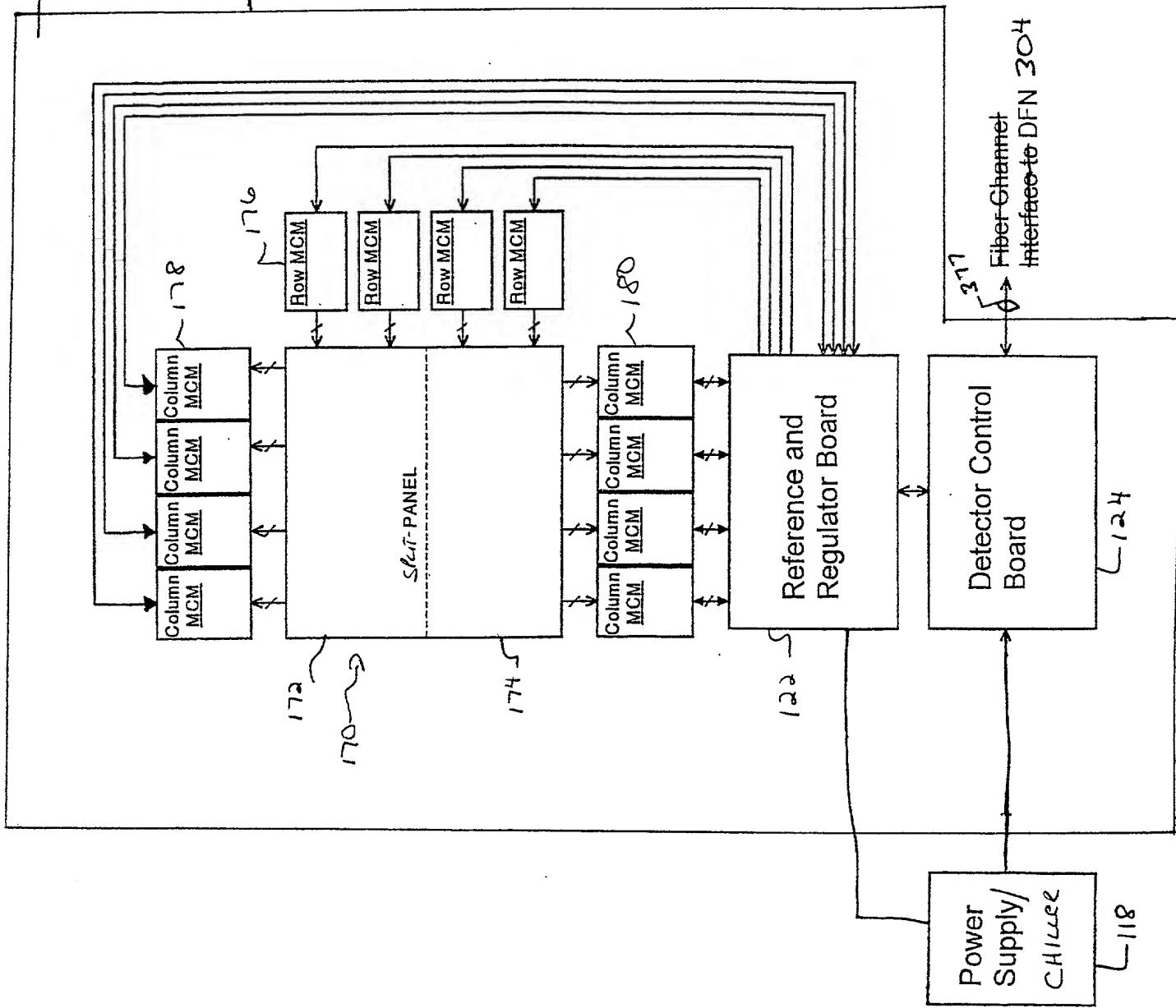
FIG. 5
(prior $\Delta\tau$)

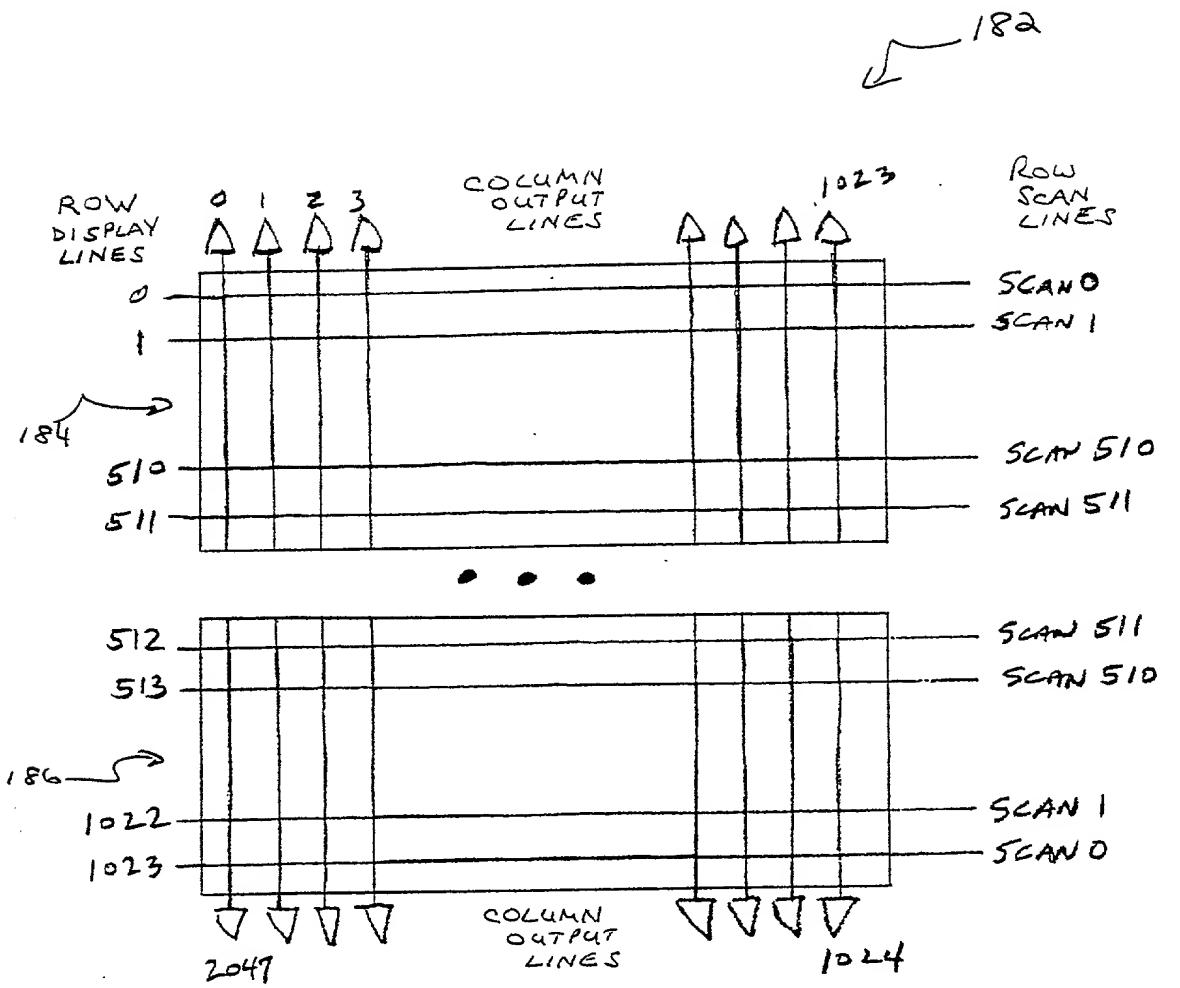


FLAT PANEL DETECTOR



FLAT PANEL DETECTOR





CARDIAC/SURGICAL DIGITAL X-RAY PANEL

FIG. 8
(PRIOR ART)

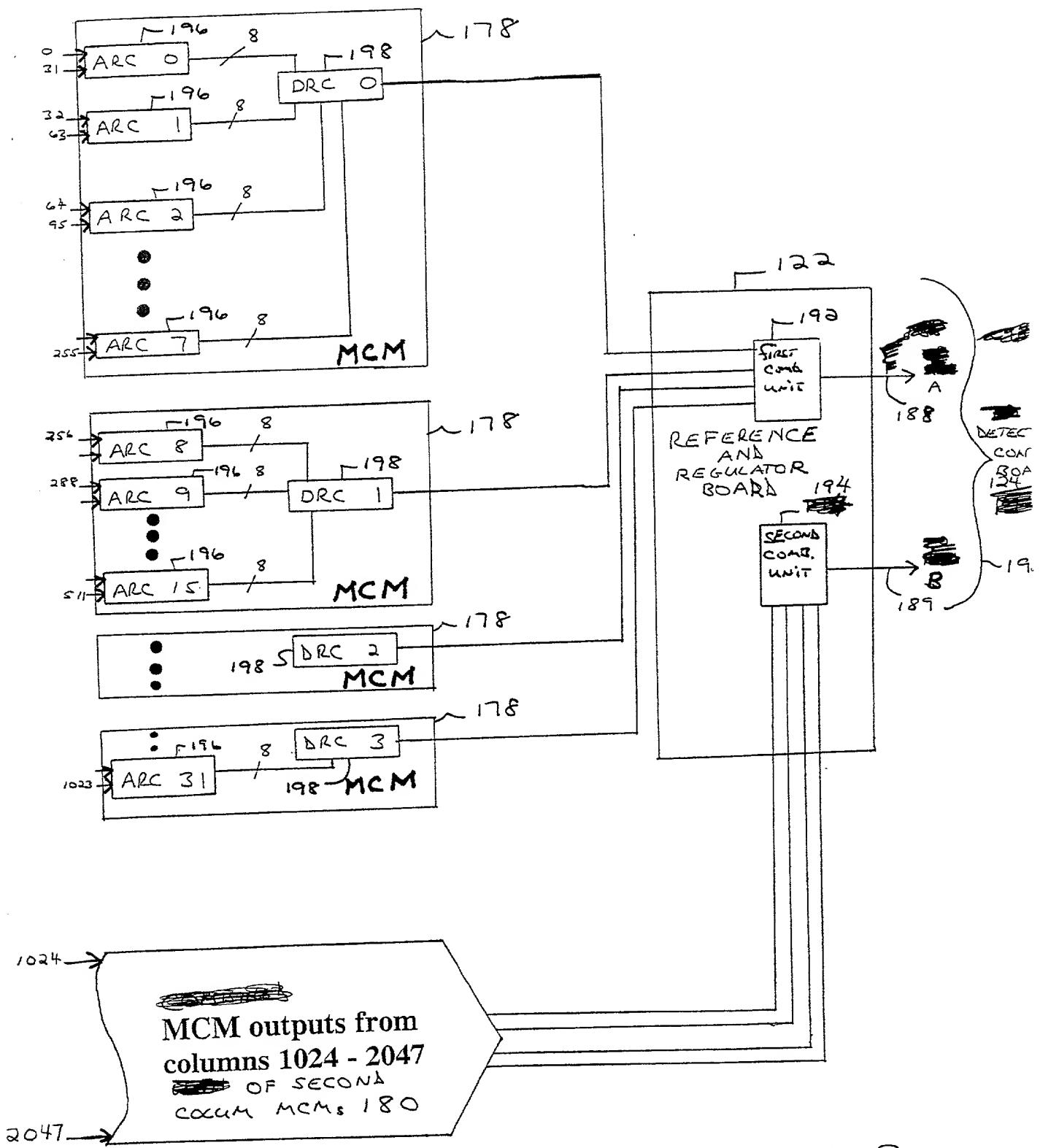
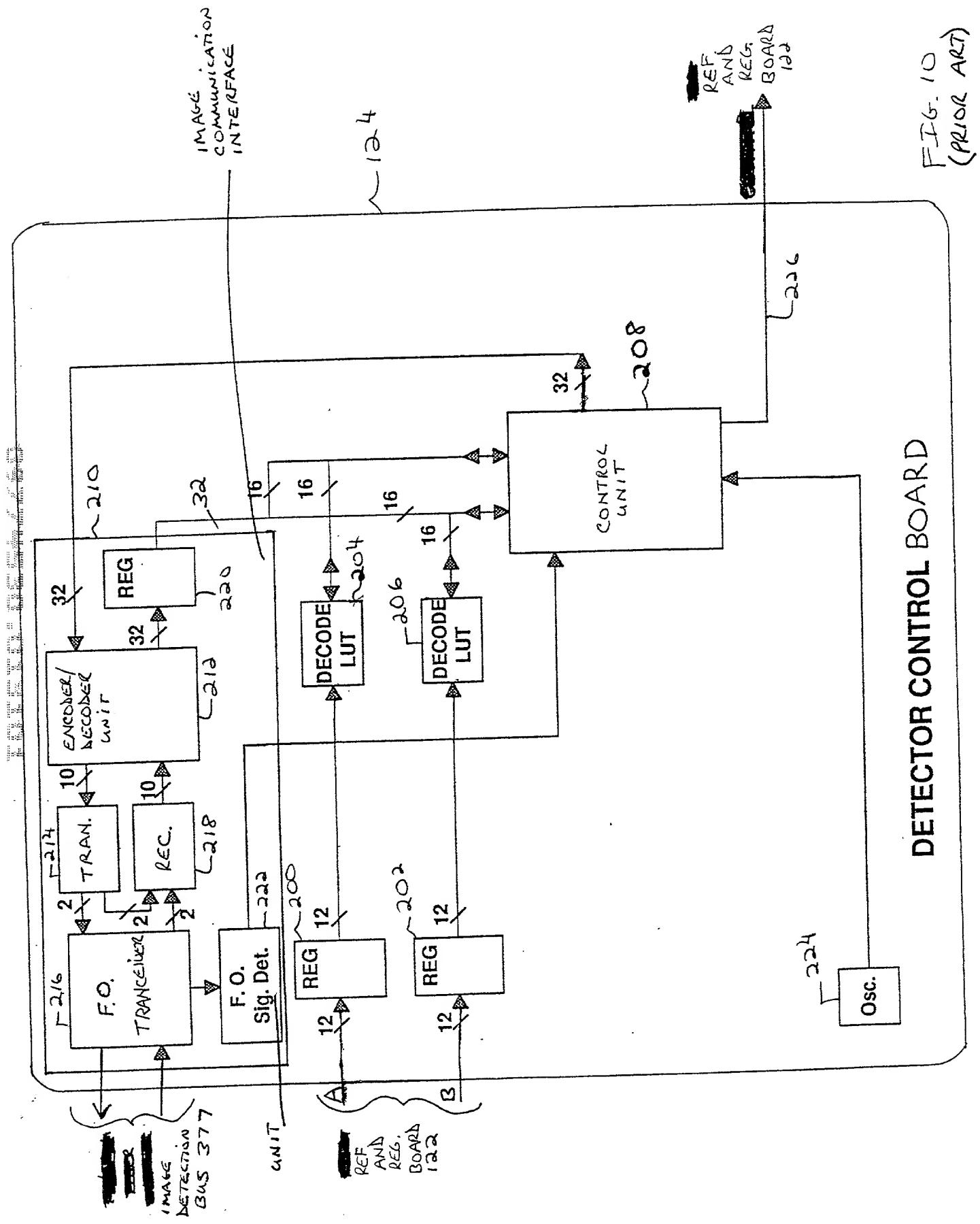
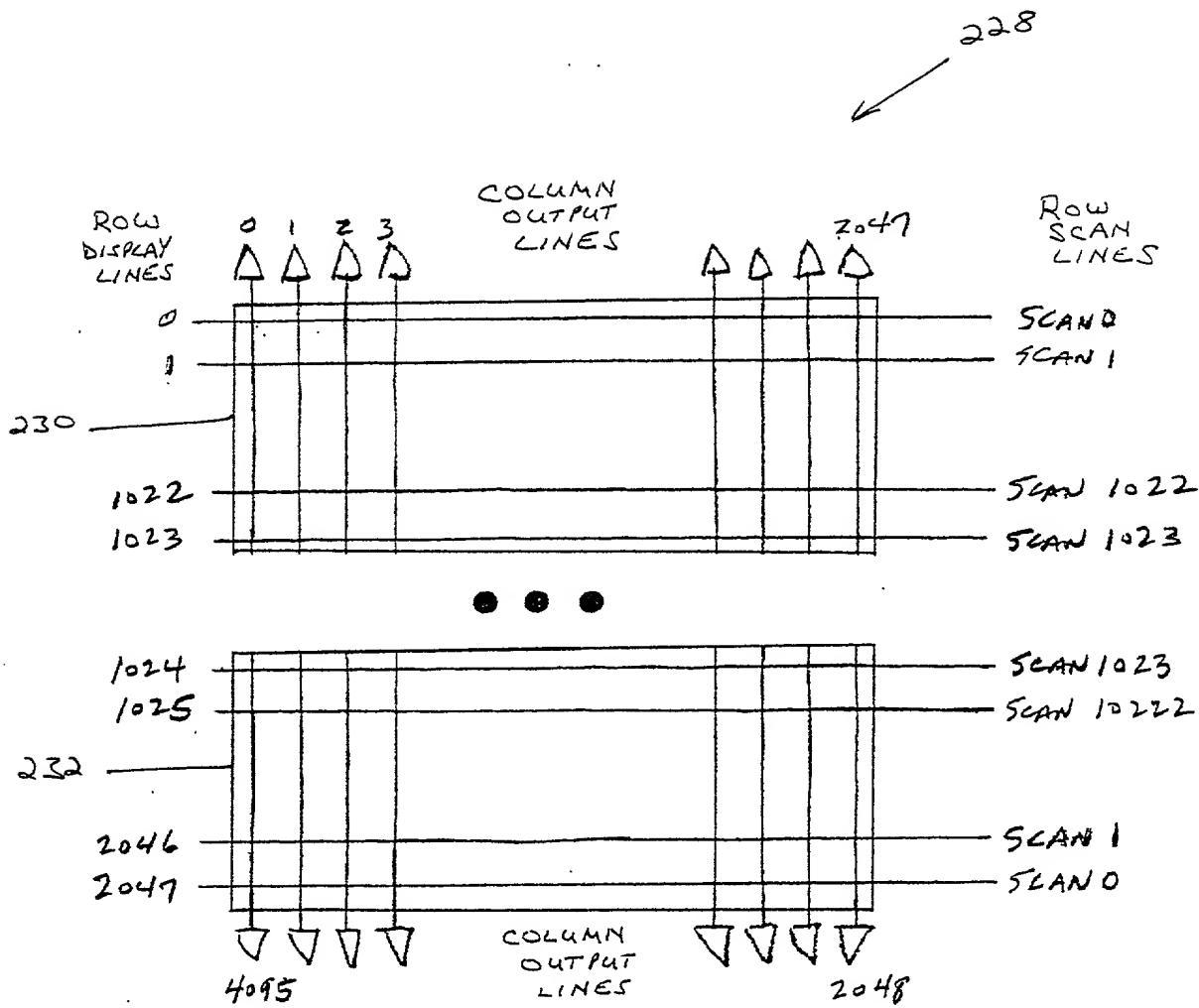


FIG. 9
(PRIOR ART)



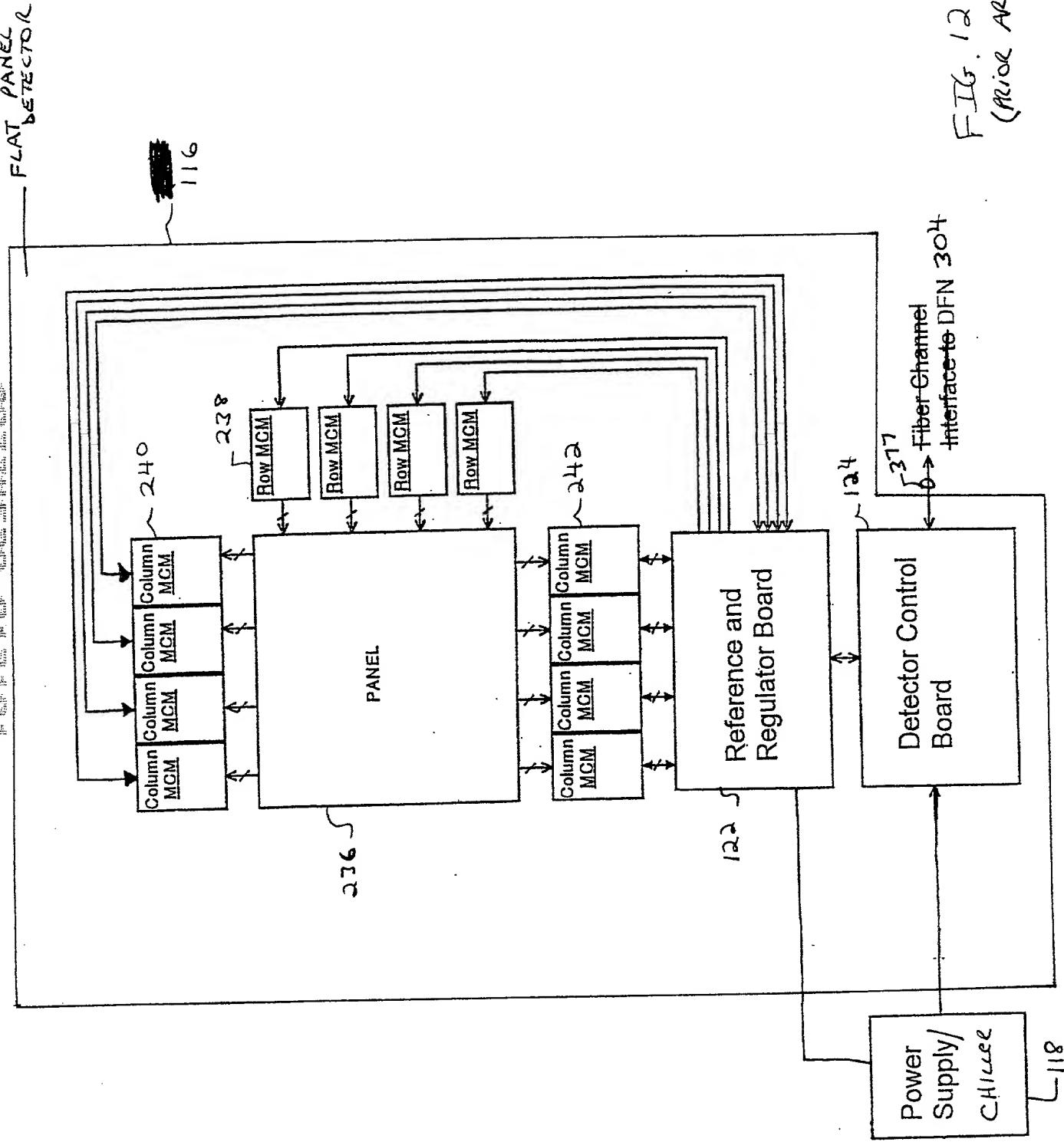
DETECTOR CONTROL BOARD

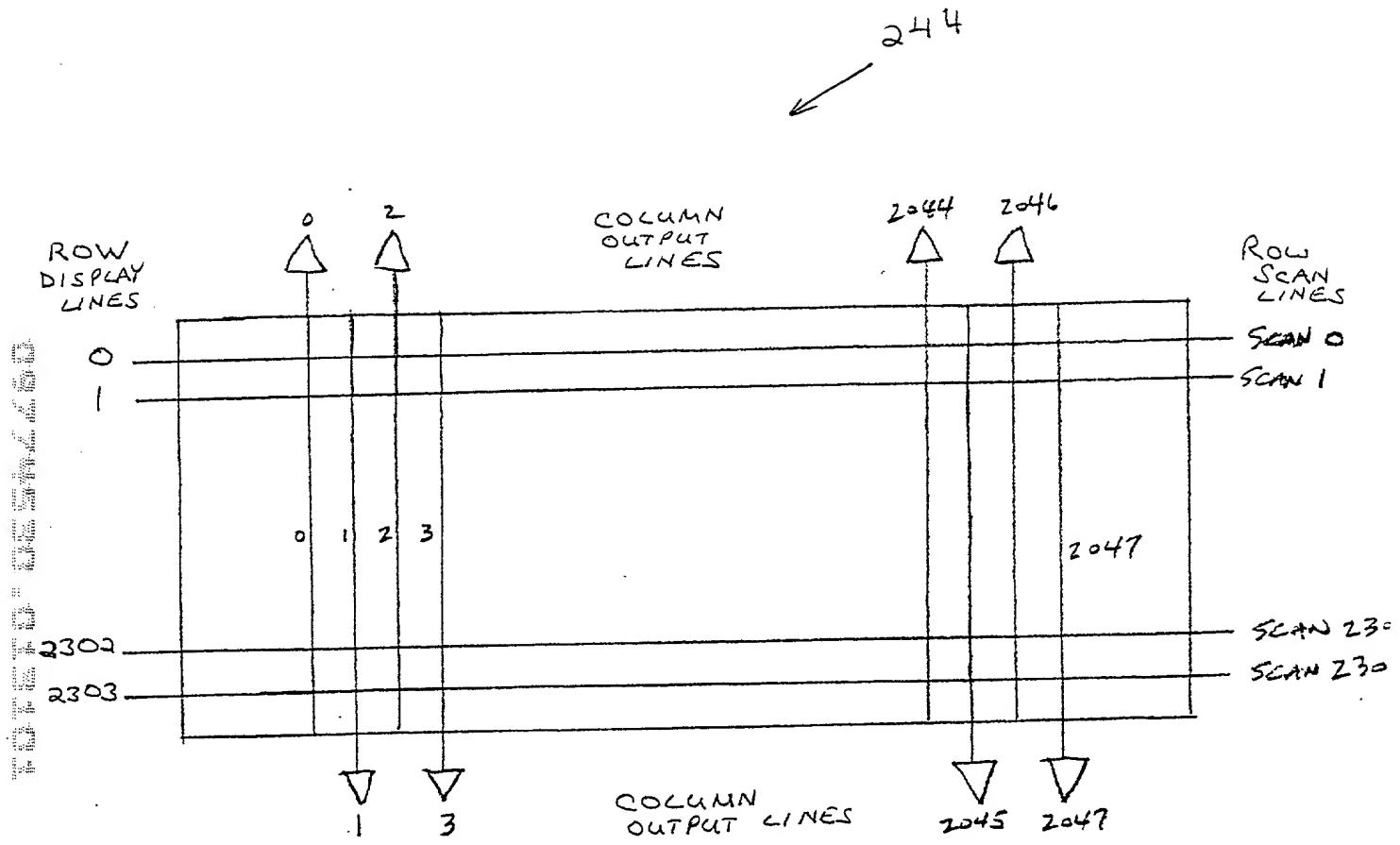
FIG. 10
(PRIOR ART)



RADIOGRAPHY DIGITAL X-RAY PANEL

FIG. 11
(PRIOR ART)





MAMOGRAPHY DIGITAL X-RAY PANEL

FIG. 13
(PRIOR ART)

FLAT PANEL
DETECTOR

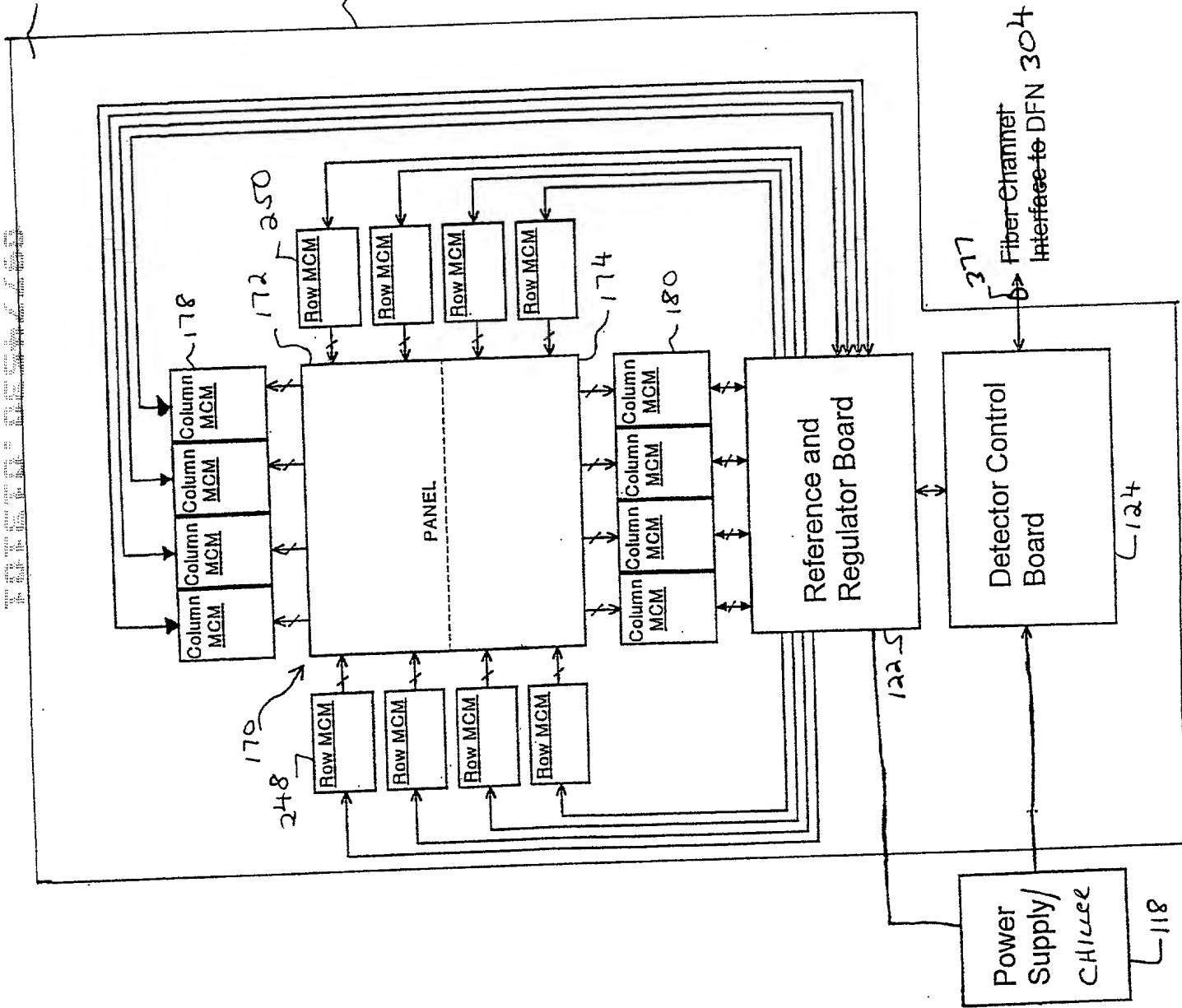


FIG. 14
(Pulse Art)

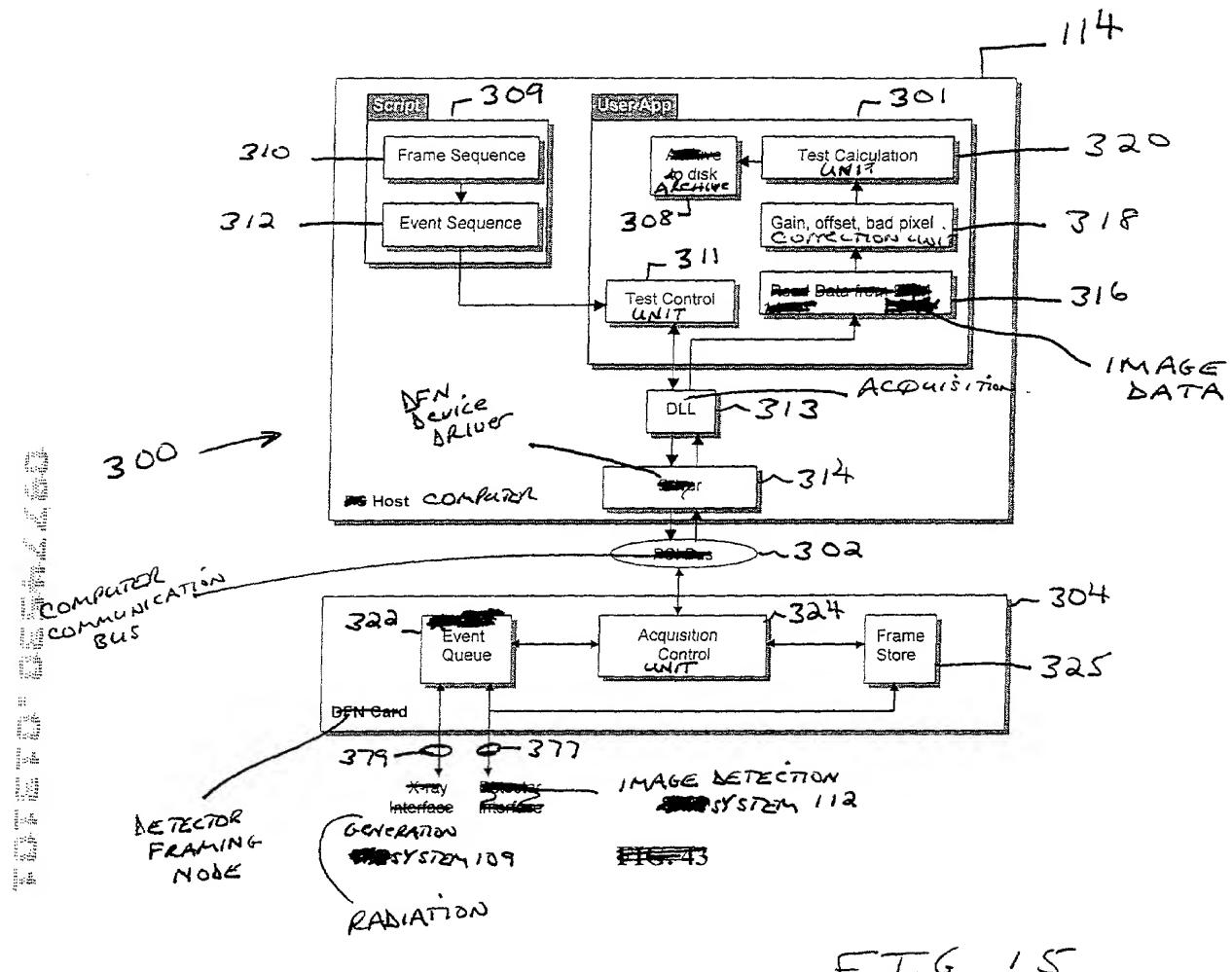


FIG. 15

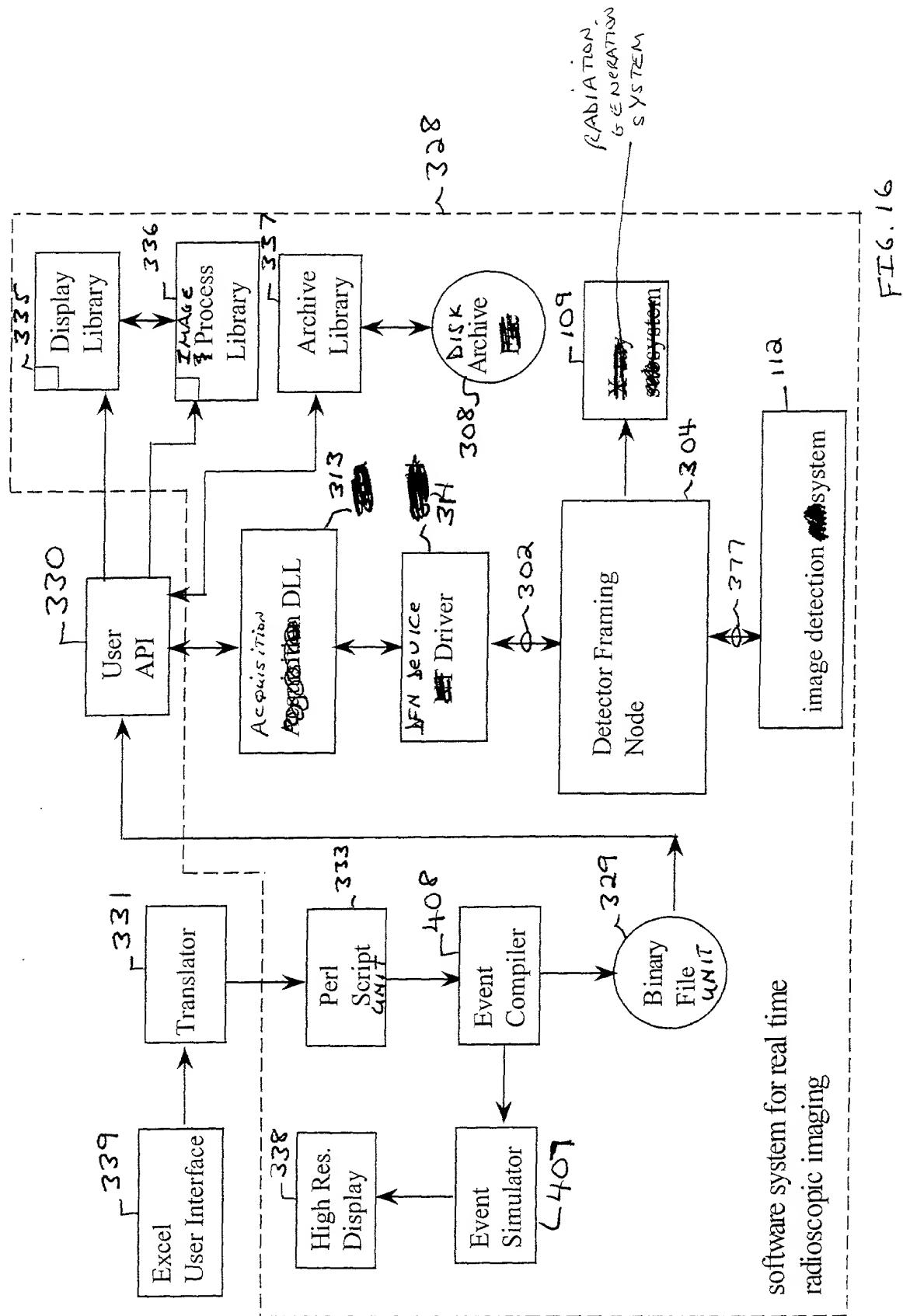


FIG. 16

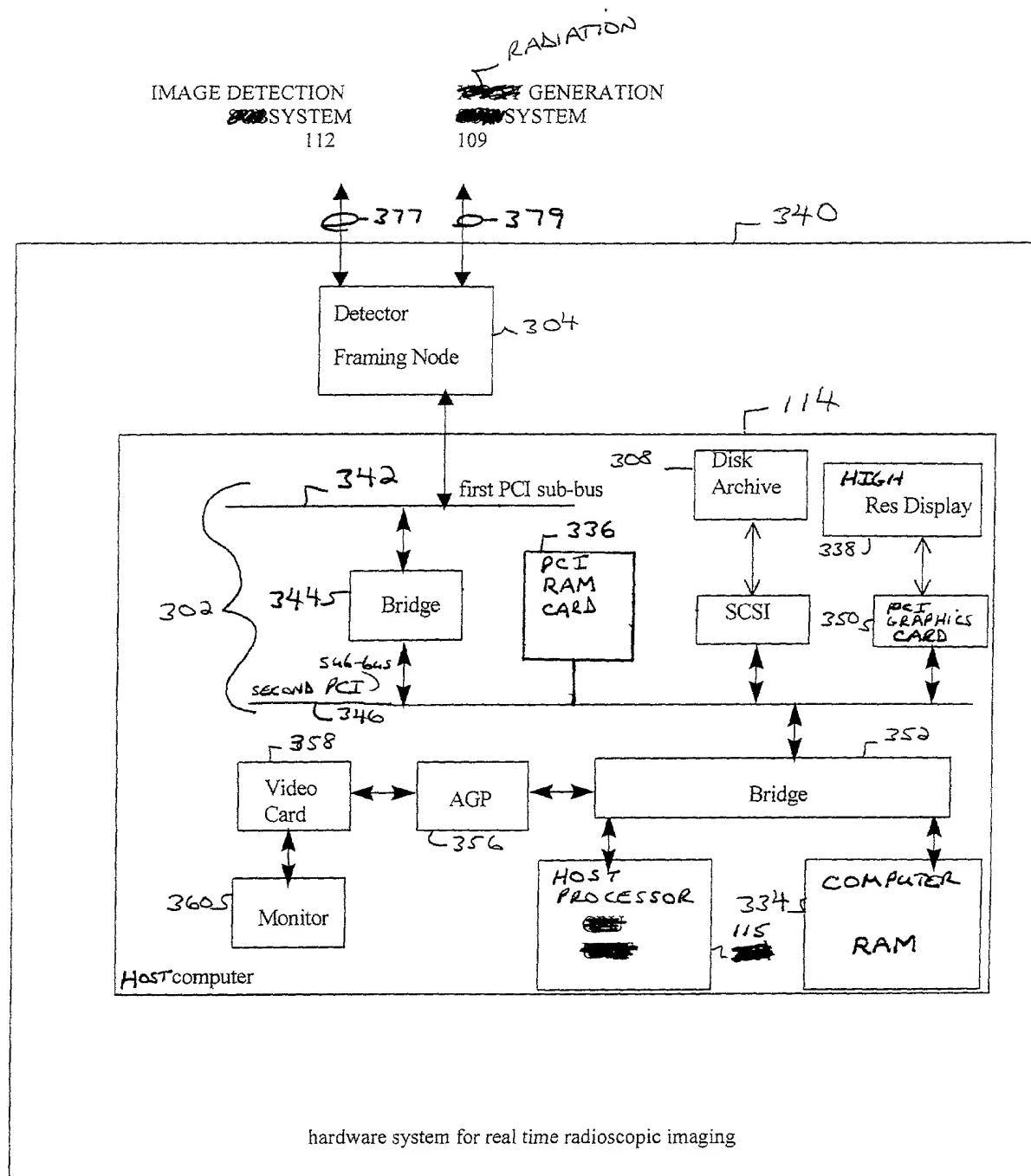
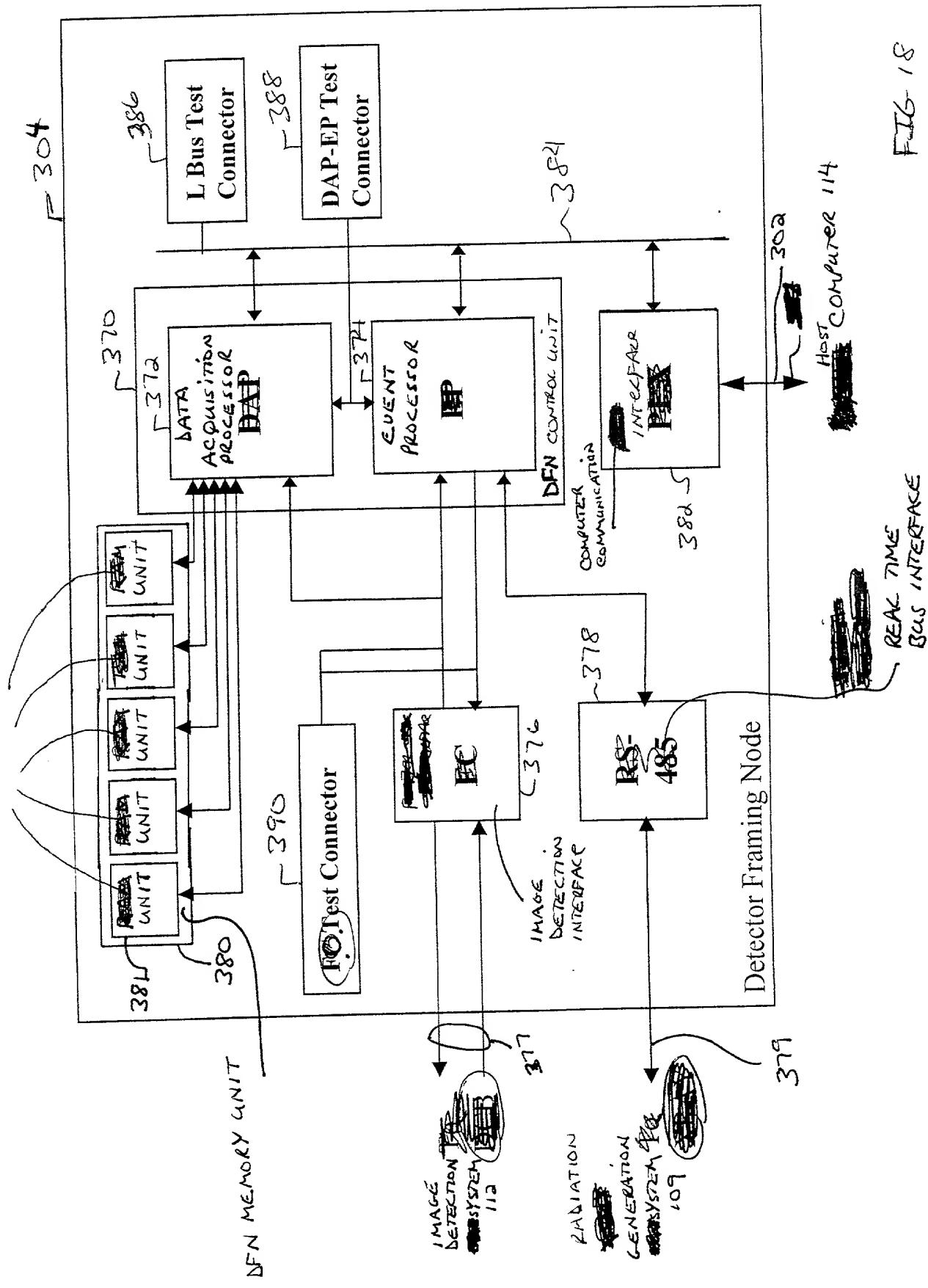


FIG. 17



11:1346 / DAB-EN CASES\DEALINGS\Bwg-01 (proc 2)

| Panel | Setup | Real Time | Length (fm/sec) | Length | Latency | memory | offset | gb/r |
|--------------|--------------|-----------|-----------------|-----------|---------------|--------|--------|------|
| Single Frame | Post Process | - | 30 | unlimited | < 5 frames | host | none | |
| Single Frame | Post Process | - | - | - | Delay ~.1 sec | " | y | |
| Real Time | Real Time | R | R | Unlimited | < 5 frames | " | y | |
| Real Time | Real Time | R - X | R - X | Unlimited | < 5 frames | host | none | |
| Real Time | Real Time | R - Y | R - Y | Unlimited | < 5 frames | " | y | |
| | | | | | | y | y | |

FIG. 19

| Modality | Image size | Frames Stored | |
|----------|-------------|---------------|--------|
| | | host memory | memory |
| Cardiac | 1024 x 1024 | 200 | |
| Rad | 2048 x 2048 | 50 | |
| Mammo | 2304 x 2048 | 44 | |

FIG. 20

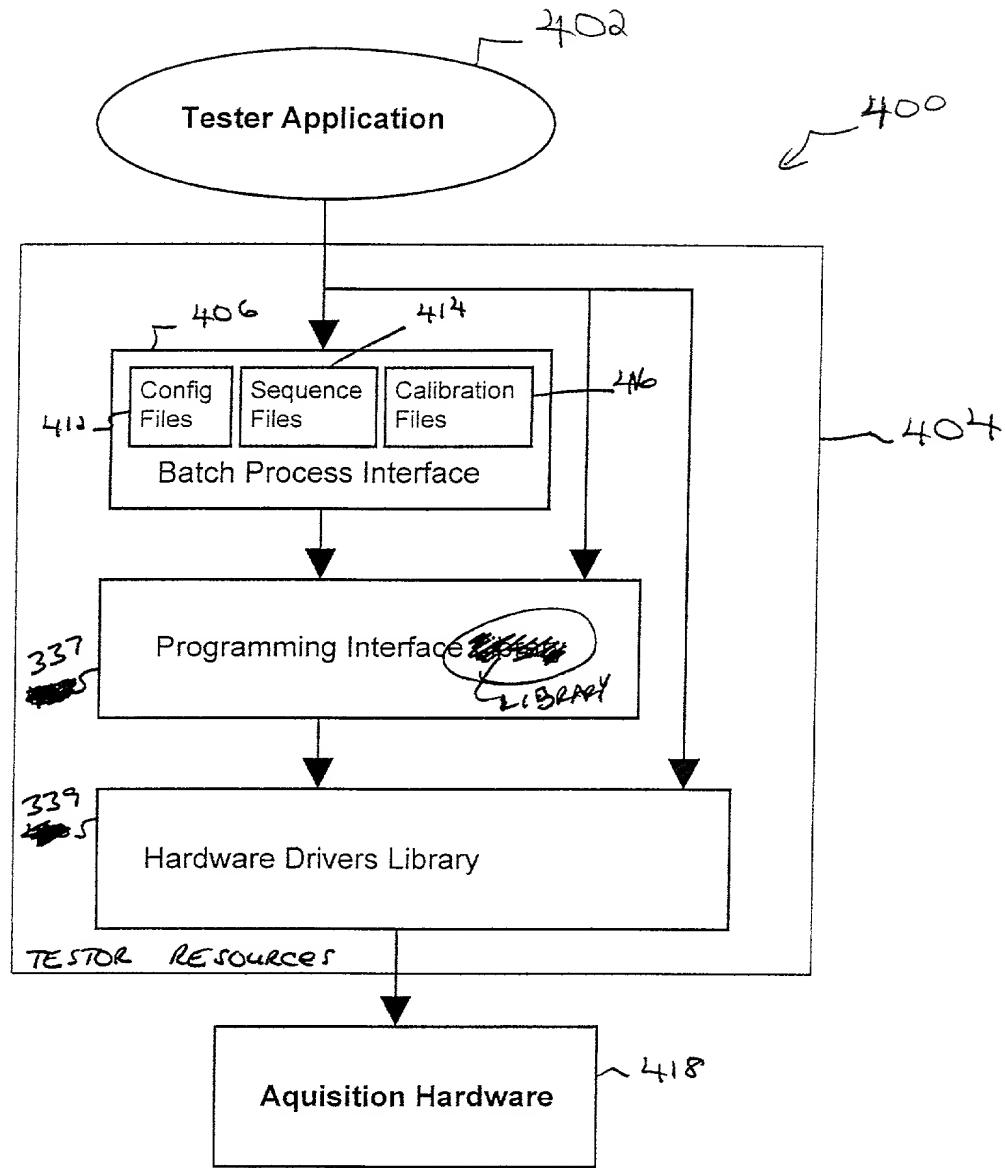
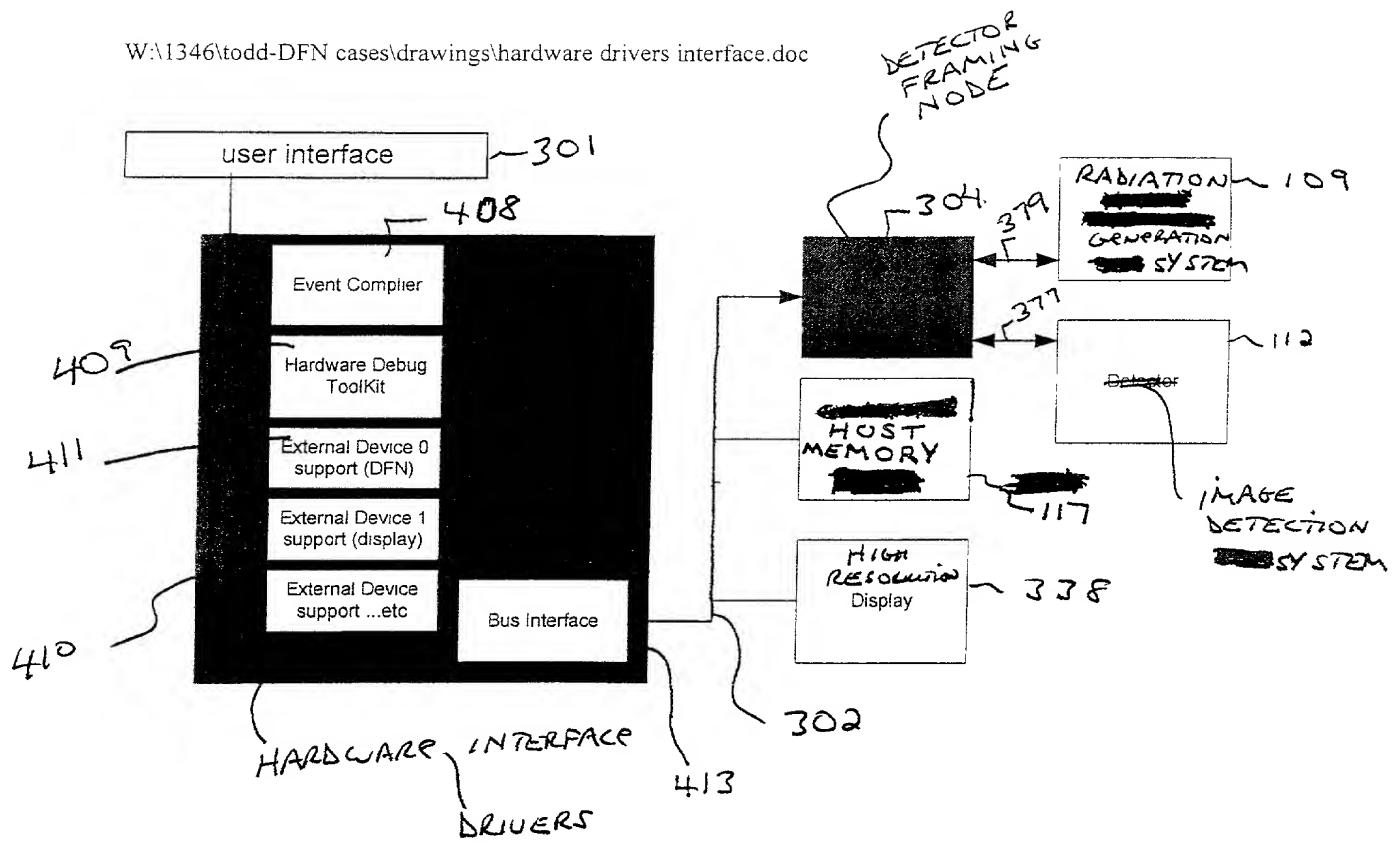


FIG. 21



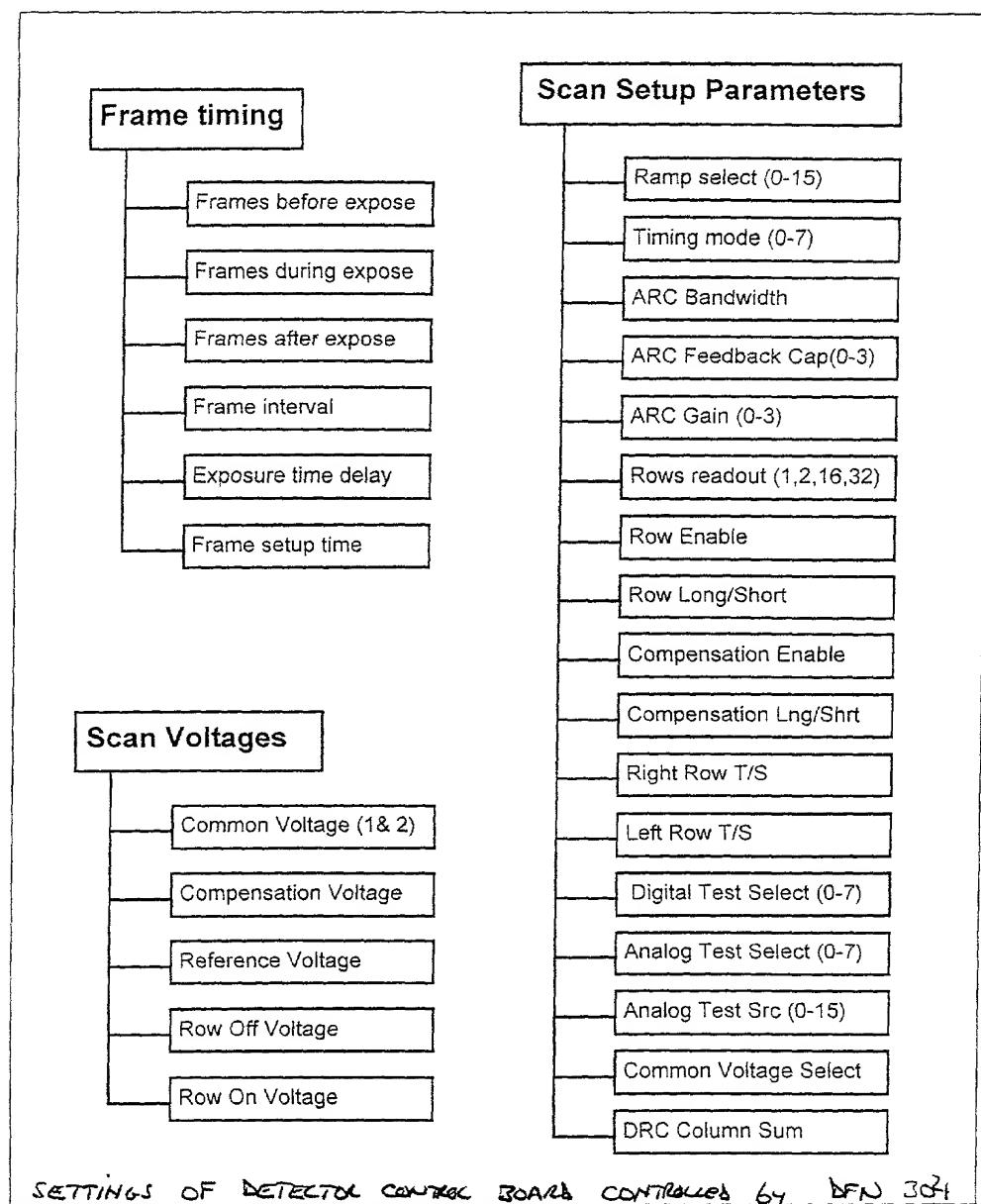


FIG. 23

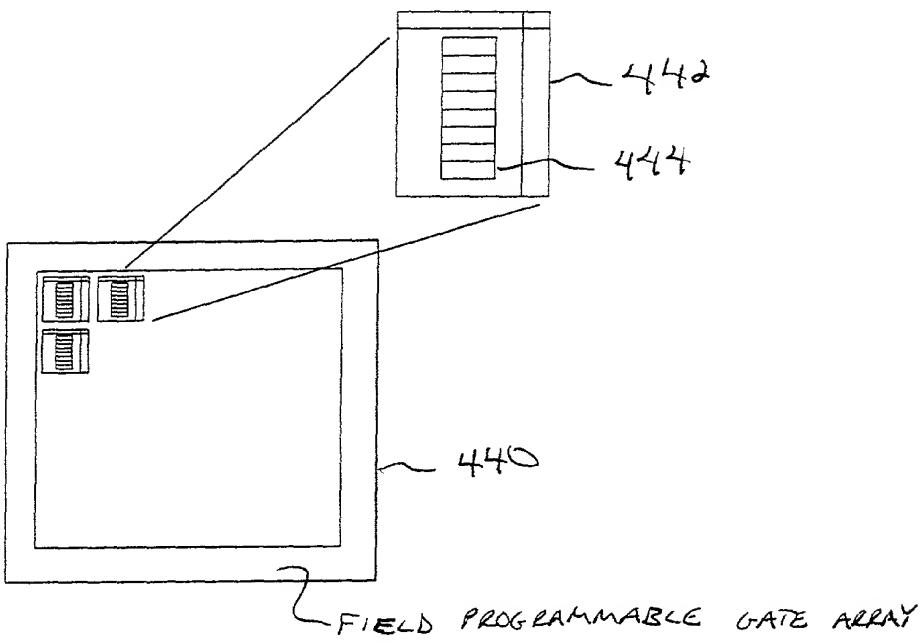


FIG. 24

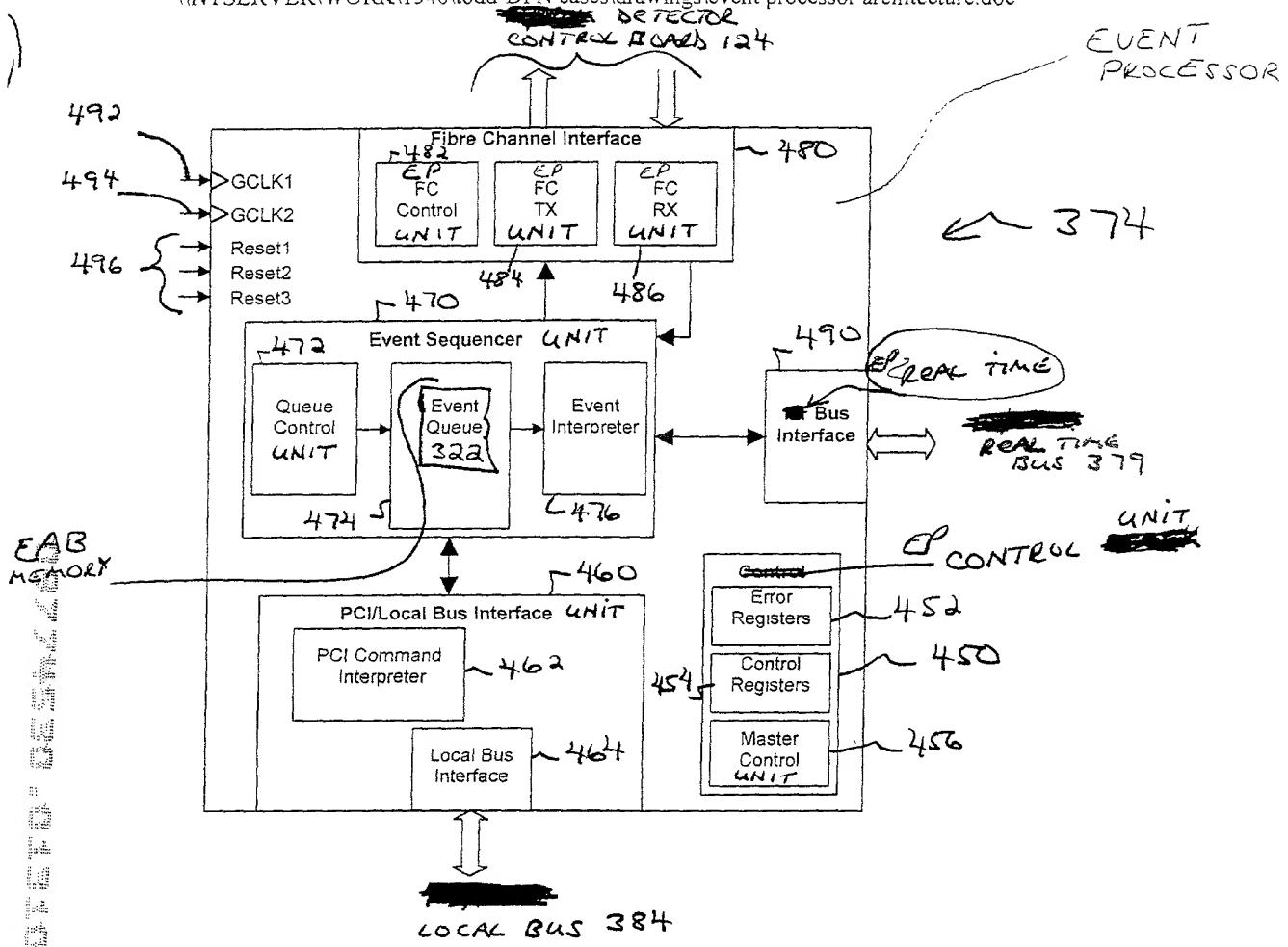


FIG. ~~25~~ 25

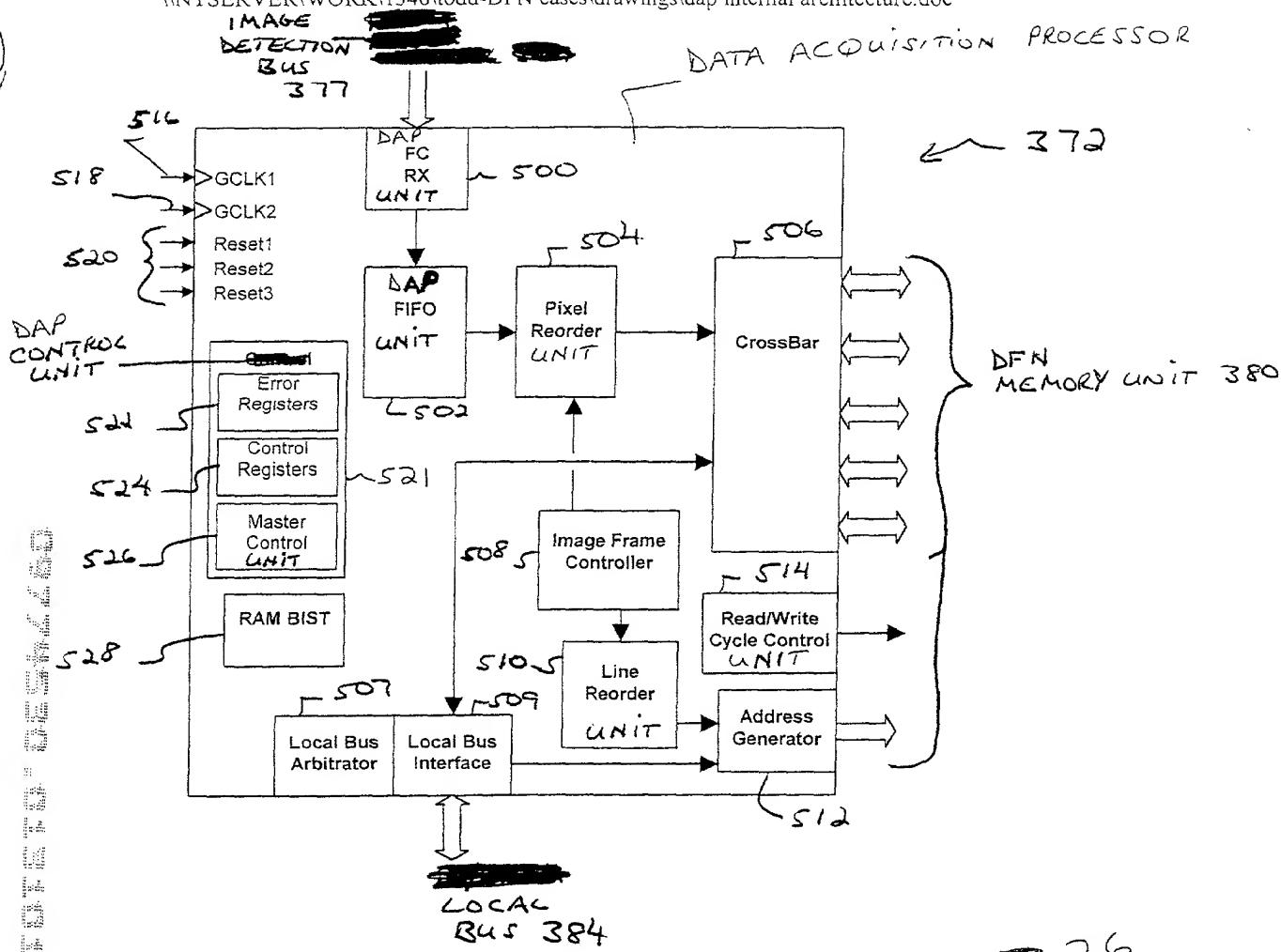


FIG. 26

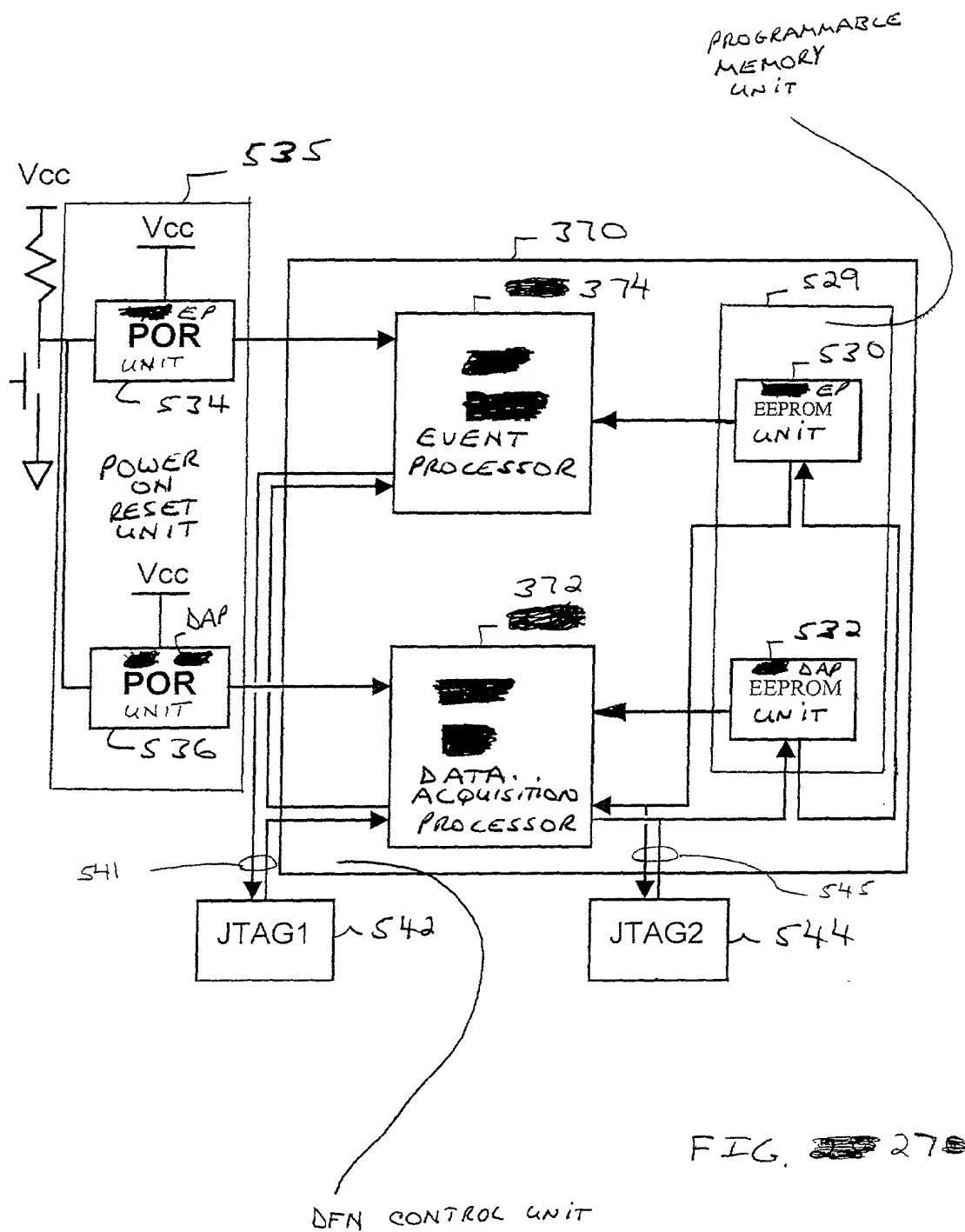
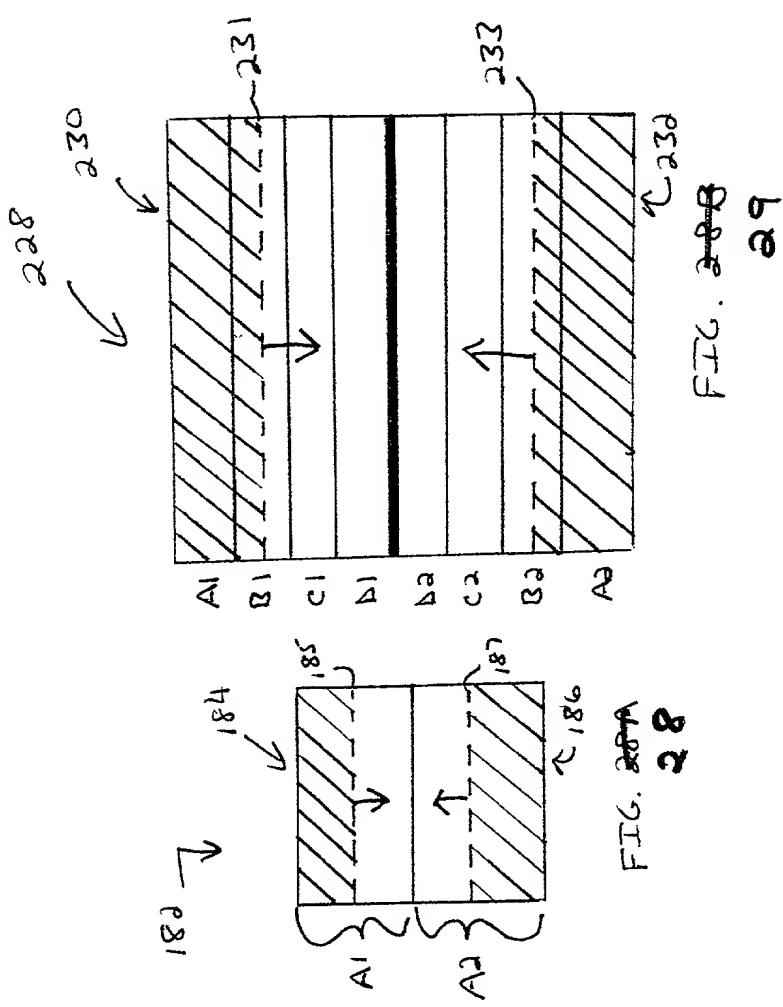


FIG. 27



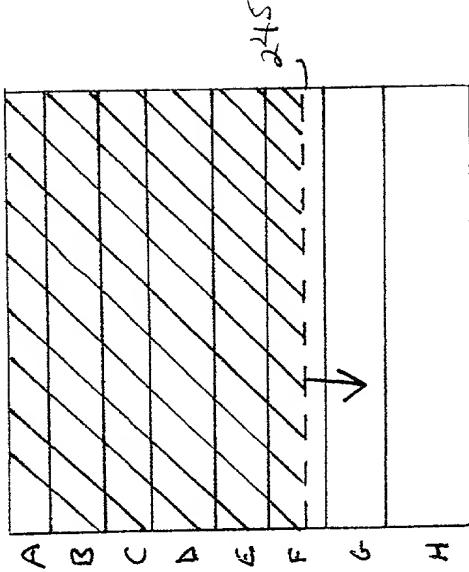
228
230

231
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235
236

FIG. 30



244

245

FIG. 30

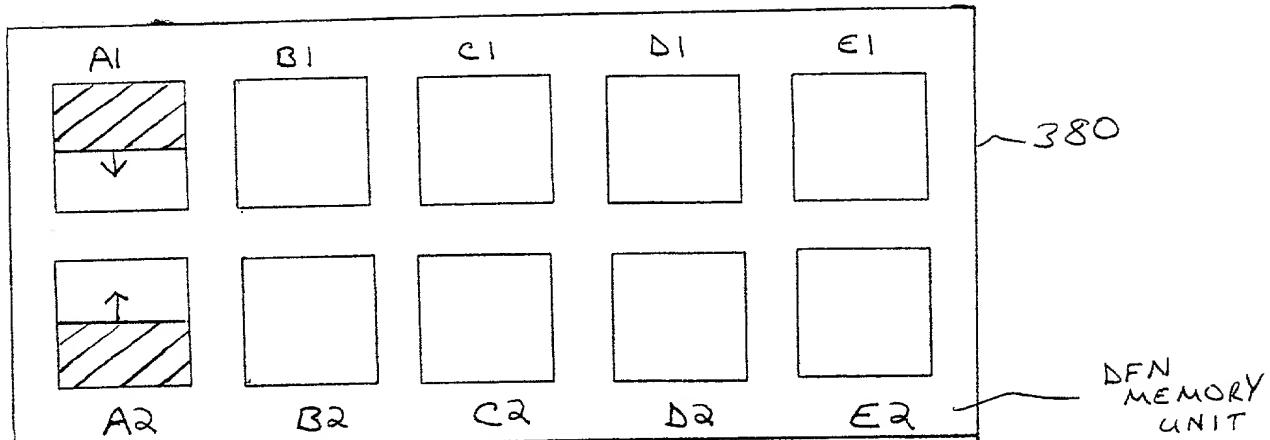


FIG. ~~29~~ 31

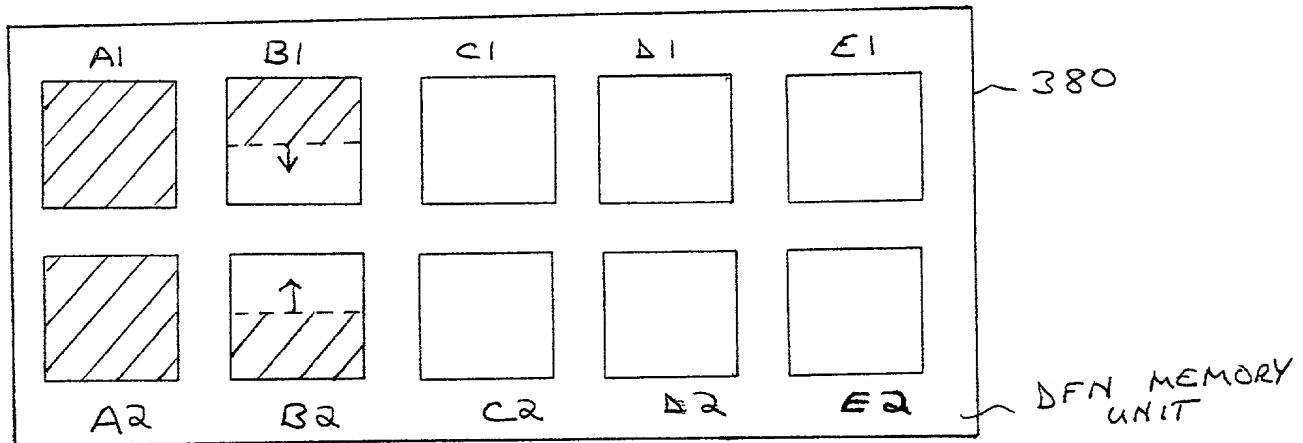


FIG. ~~29~~ 32

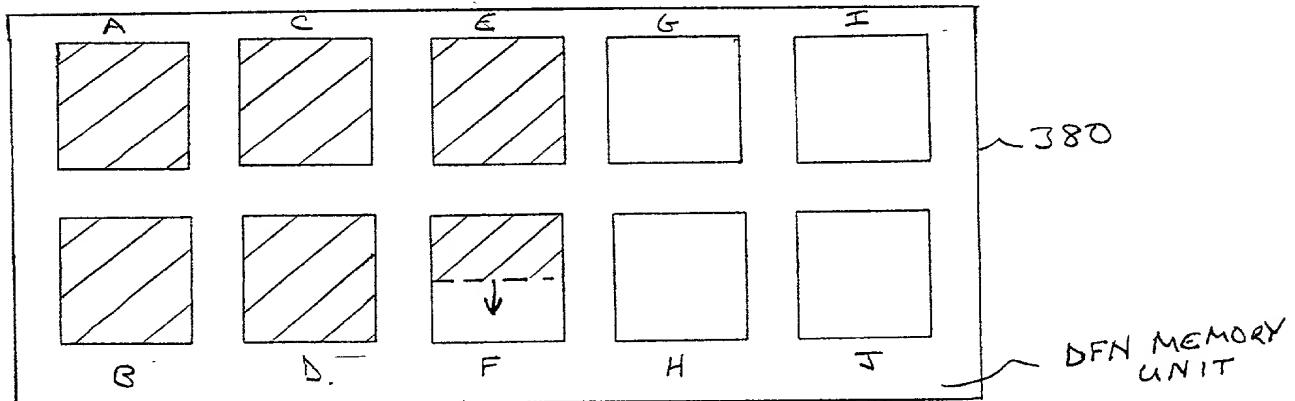


FIG. ~~29~~ 33

334

| | |
|----|----|
| A1 | B1 |
| C1 | |
| A1 | |
| A2 | |
| B2 | |
| A2 | |

FIG. 34

334

| | |
|---|--|
| A | |
| B | |
| C | |
| D | |
| E | |
| F | |
| G | |
| H | |

FIG. 35

FIG. 36

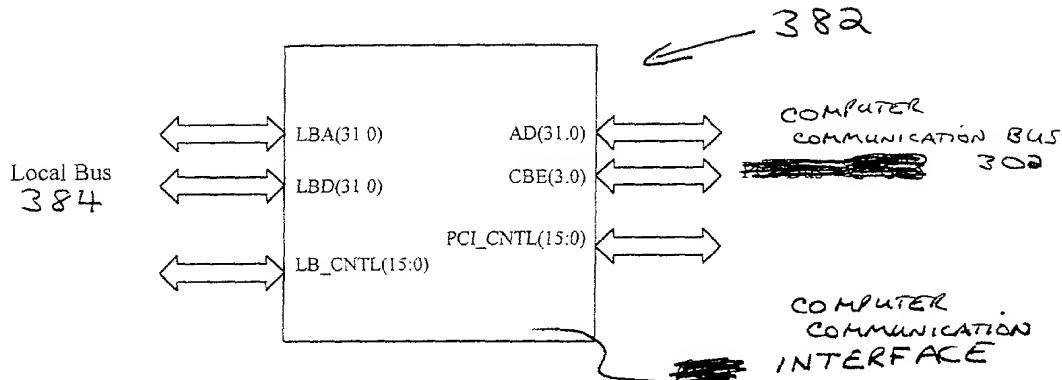


FIG. 37